

Online Parallel Paging and Green Paging

Kunal Agrawal
Washington U

Michael Bender
Stony Brook

Rathish Das
Waterloo → Liverpool

William Kuszmaul
MIT

Enoch Peserico
U Padova

Michele Scquizzato
U Padova



That's 6 words.



That's 5 words.

Online Parallel Paging and Green Paging

Kunal Agrawal
Washington U

Michael Bender
Stony Brook

Rathish Das
Waterloo → Liverpool

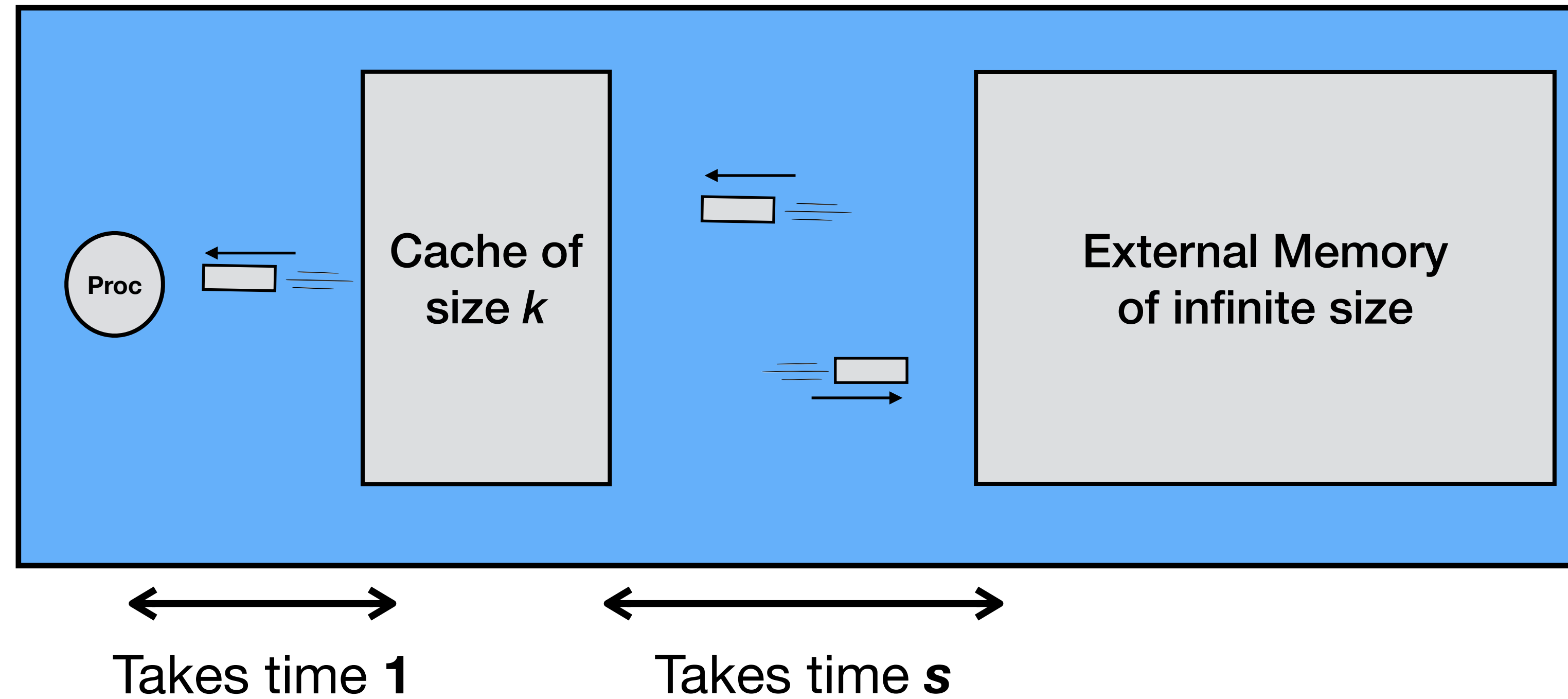
William Kuszmaul
MIT

Enoch Peserico
U Padova

Michele Scquizzato
U Padova

Processor makes page/block requests:

$r_1, r_2, r_3, r_4, r_5, r_6, r_7, r_8, \dots$



- Up to k pages can be kept in cache at a time.
- **Algorithmic decision:** control which pages are moved in/out of cache.

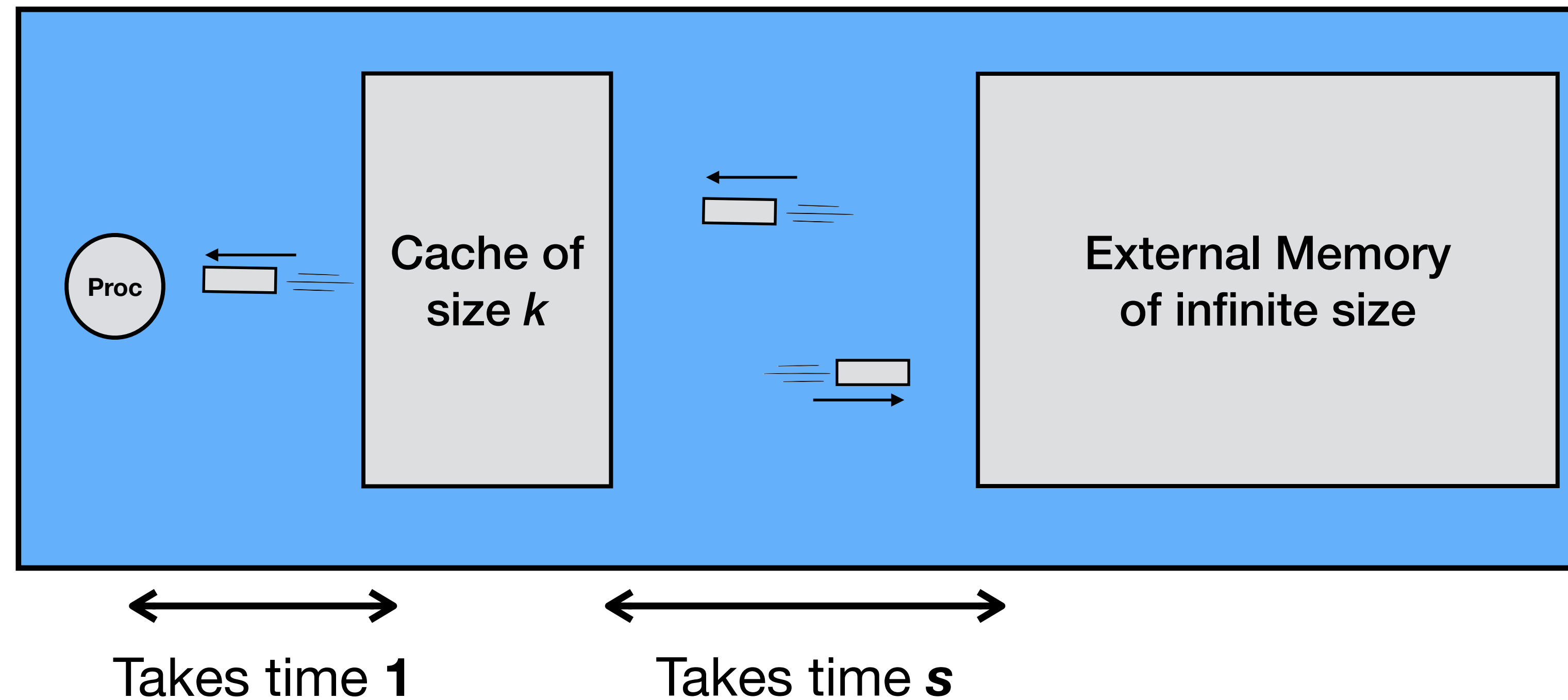
Goal: Complete the request sequence as fast as possible.

Classical (Offline) Sequential Paging

[Beladi 66], [Mattson, Gecsei, Slutz, Traiger 70],
[Sleator, Tarjan 85], [Borodin, El-Yaniv 98]

Processor makes page/block requests:

$r_1, r_2, r_3, r_4, r_5, r_6, r_7, r_8, \dots$

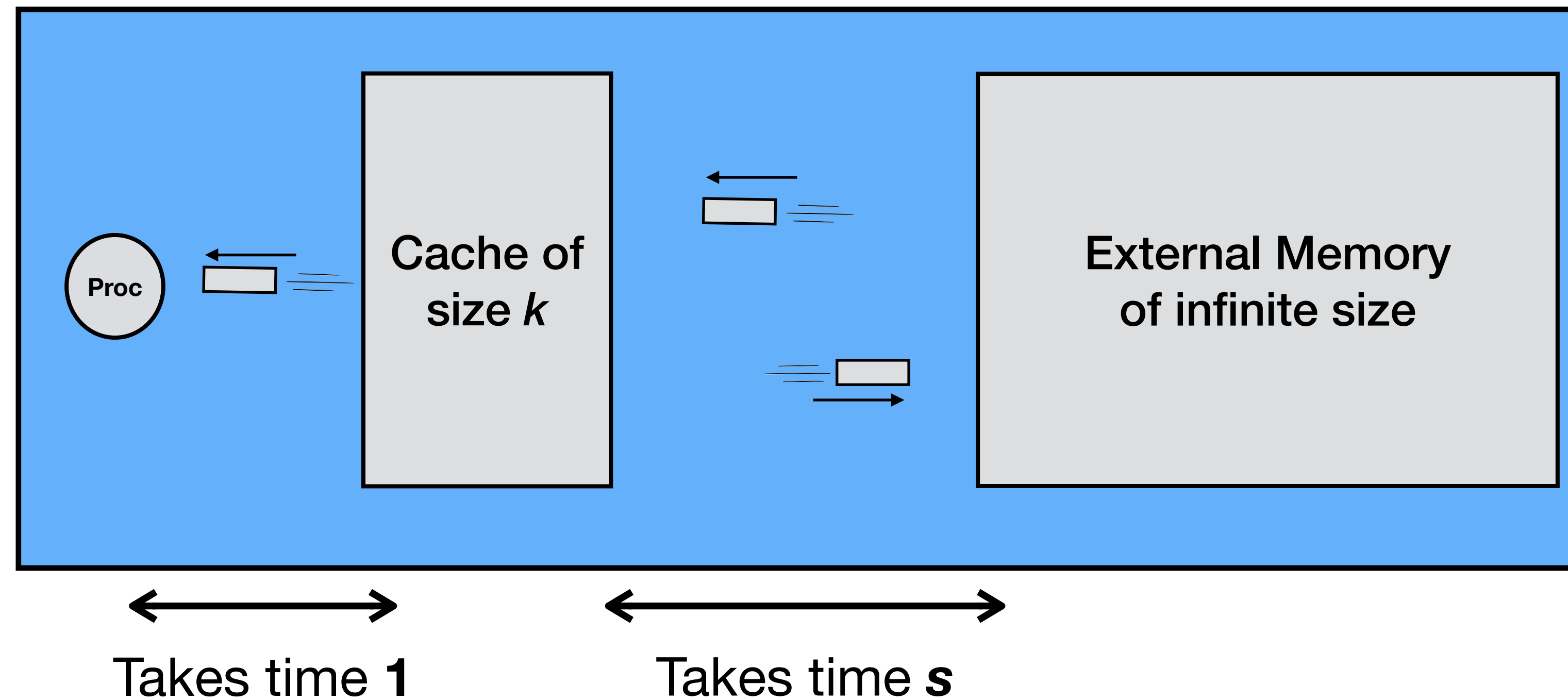


Offline Opt: Evict the page that will be used *farthest in future*.

[Beladi 66],
[Mattson, Gecsei, Slutz, Traiger 70]

Processor makes page/block requests:

$r_1, r_2, r_3, r_4, r_5, r_6, r_7, r_8, \dots$



Natural online alg: Always evict the page that was *least recently used (LRU)*.

Classical Theorem [Sleator, Tarjan 85]:

With $O(1)$ resource augmentation, LRU is $O(1)$ -competitive,

i.e., $LRU_k \leq 2 OPT_{k/2}$.

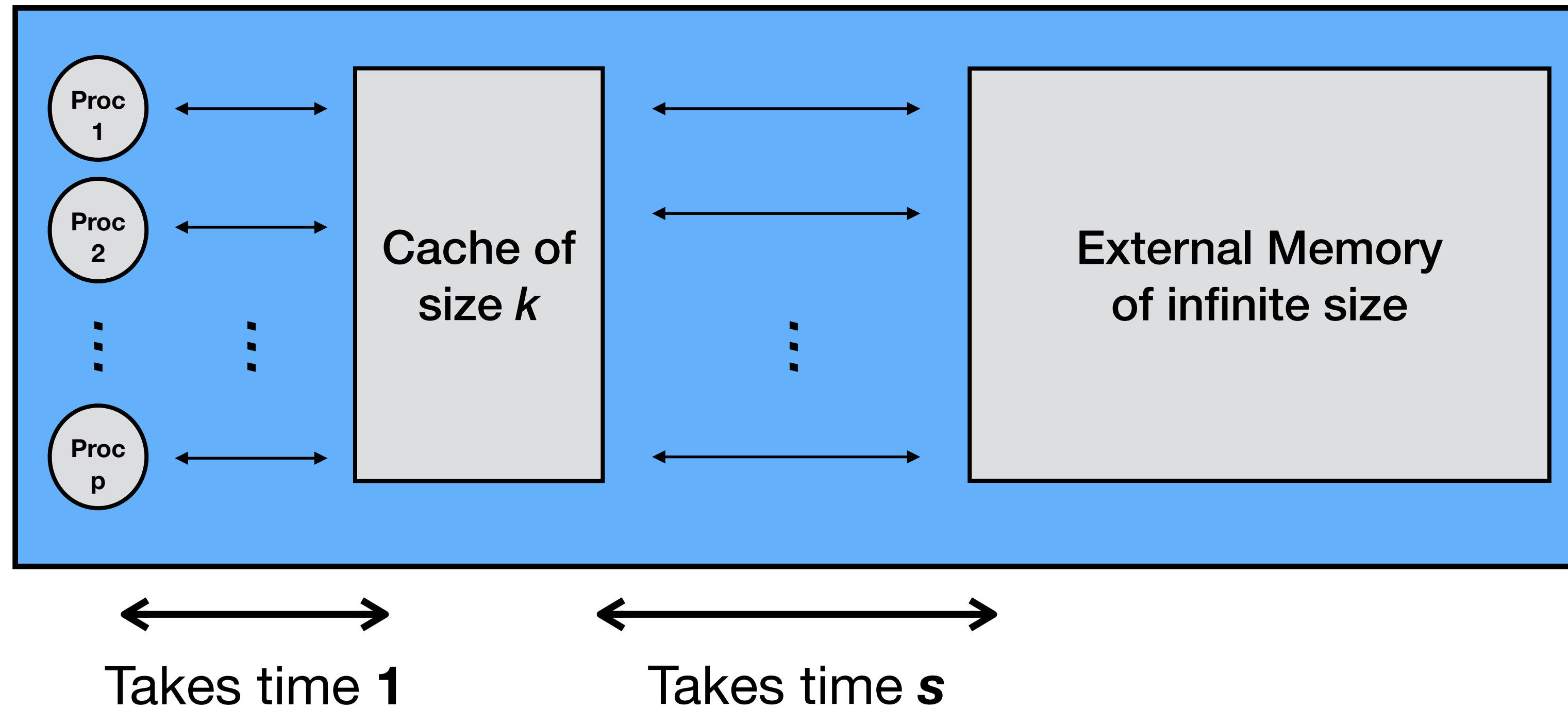
p request sequences occur in parallel:

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

⋮

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



- Different processors access disjoint sets of pages.
- Processors can access cache in parallel.
- Processors move pages between cache and external memory in parallel.

But processors must share cache of size k .

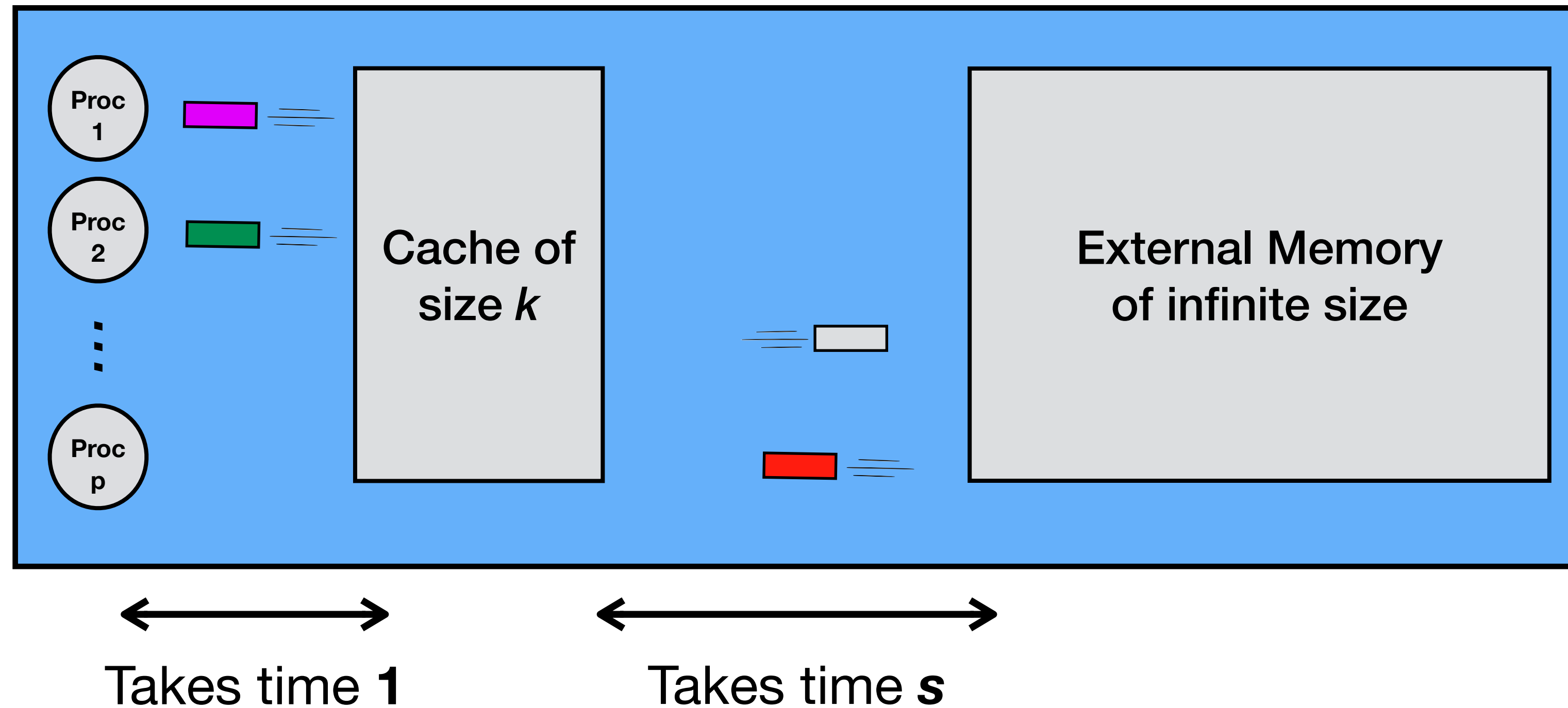
p request sequences occur in parallel:

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

⋮

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Algorithmic decision:

when a new block is brought into cache, which block should be evicted?

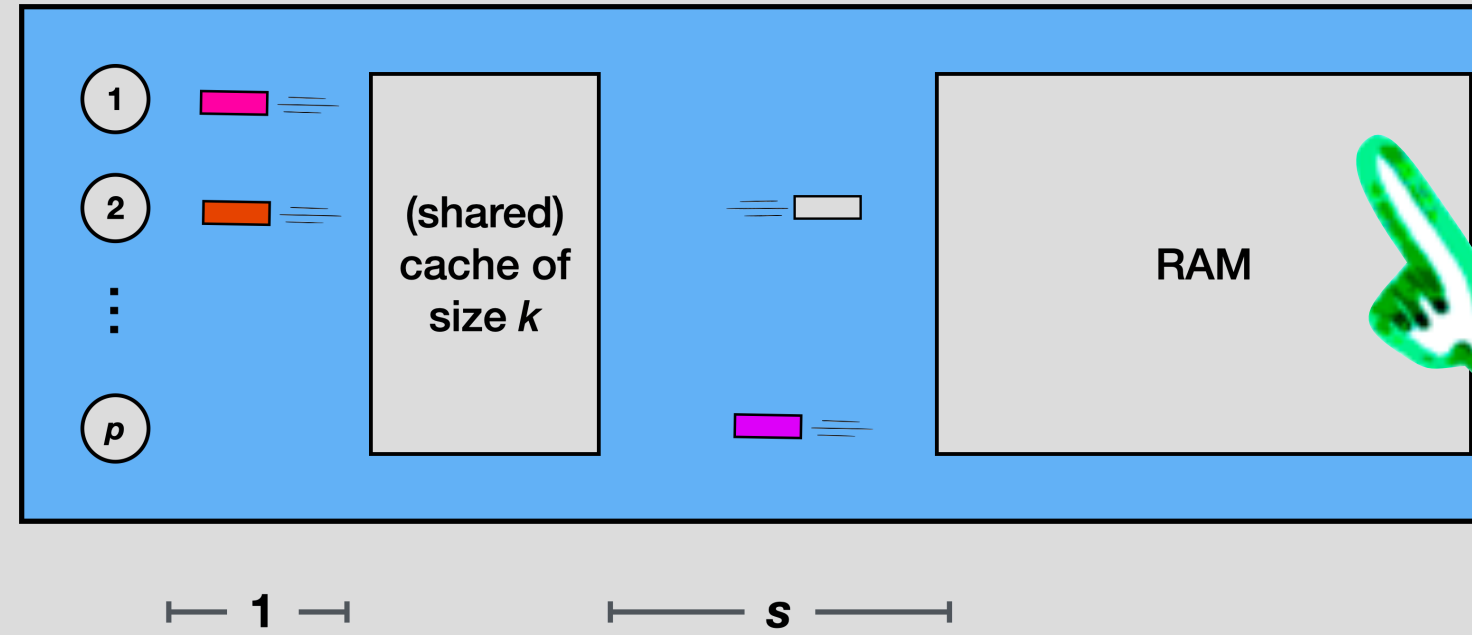
Parallel paging

p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

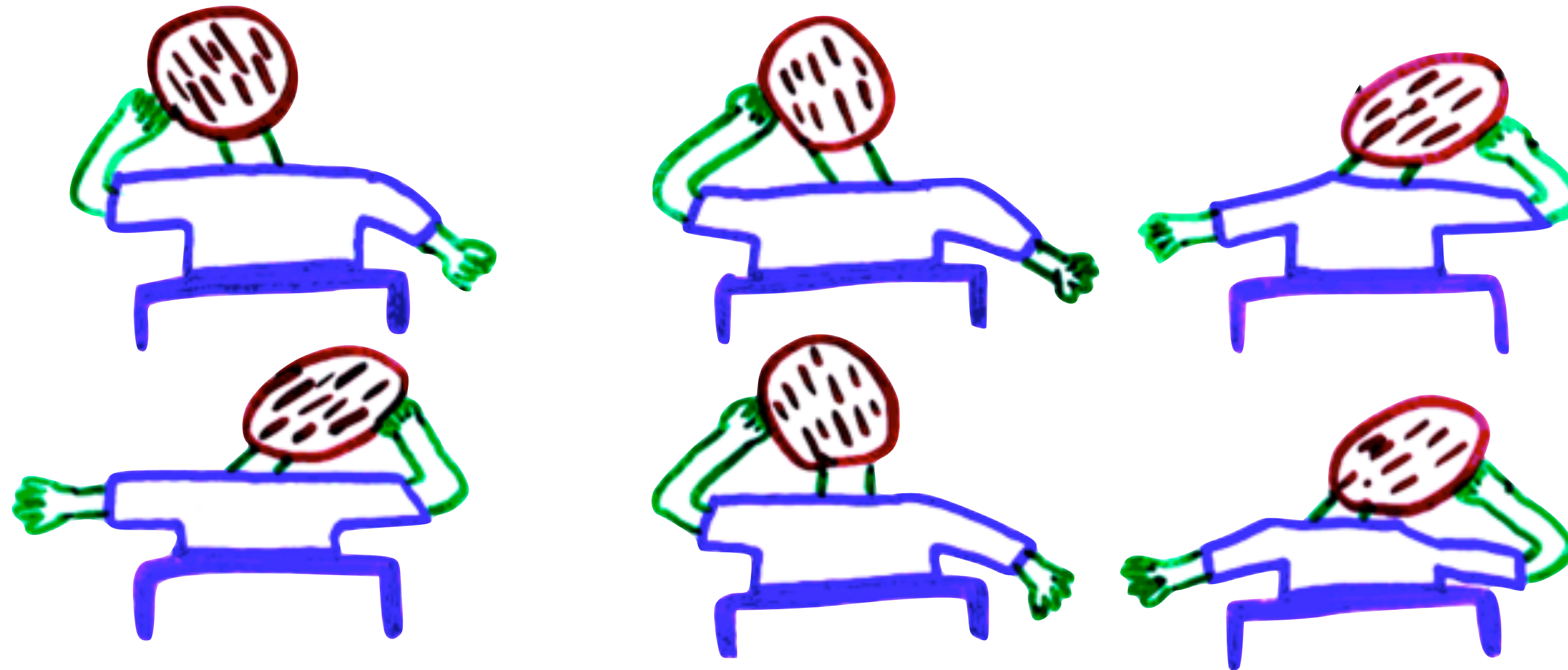
$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Performance objectives:

- makespan
- \sum completion times
- Etc.



Tight Bounds for Parallel Paging and Green Paging

Kunal Agrawal
Washington U

Michael Bender
Stony Brook

Rathish Das
Liverpool

William Kuszmaul
MIT

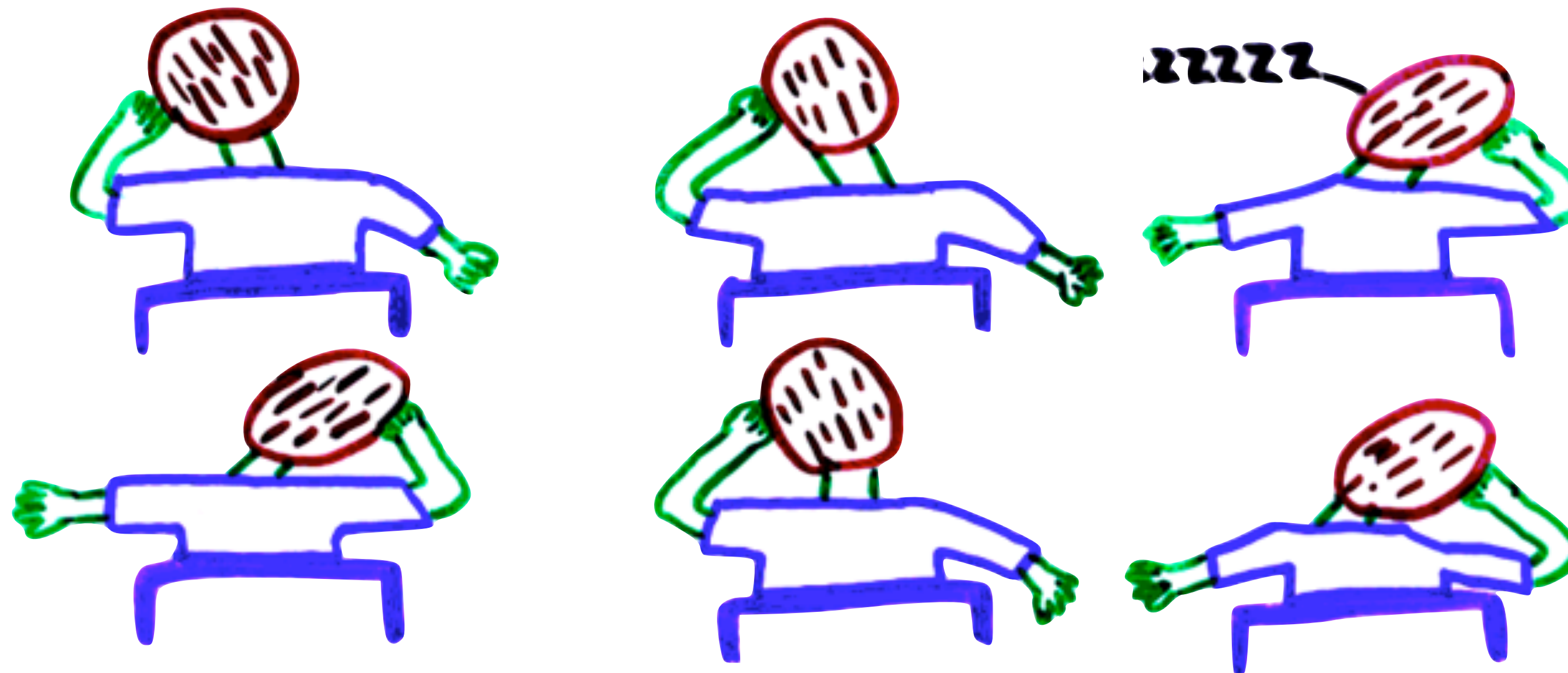
Enoch Peserico
U Padova

Michele Scquizzato
U Padova



Parallel paging is a different animal from sequential paging.

Let's talk about the challenges with *online* parallel paging.



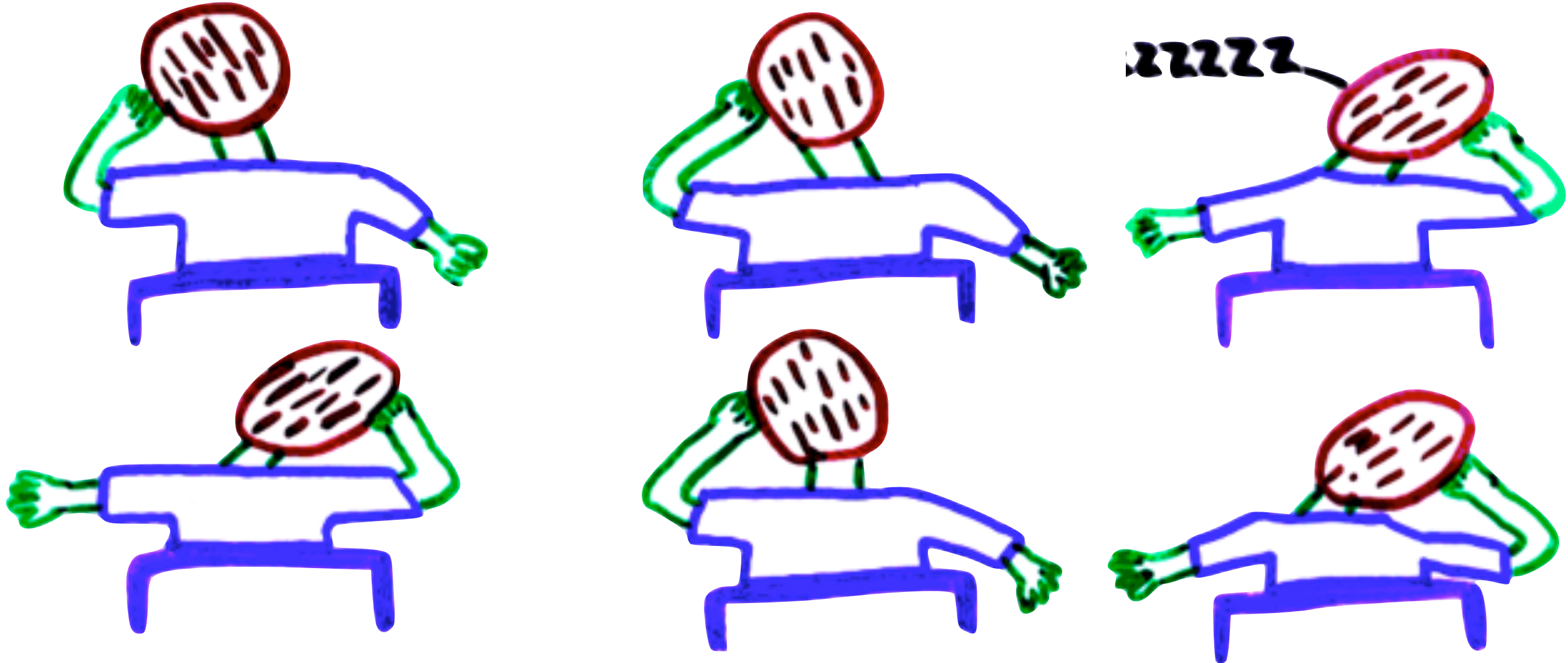
Tight Bounds for Parallel Paging and Green Paging

Kunal Agrawal Washington U	Michael Bender Stony Brook	Rathish Das Liverpool
William Kuszmaul MIT	Enoch Peserico U Padova	Michele Scquizzato U Padova

Parallel paging is a different animal from sequential paging.

Let's talk about the challenges with *online* parallel paging.

Incongruity: using the phrase "different animal" in this workshop.



Tight Bounds for Parallel Paging and Green Paging

Kunal Agrawal
Washington U

Michael Bender
Stony Brook

Rathish Das
Liverpool

William Kuszmaul
MIT

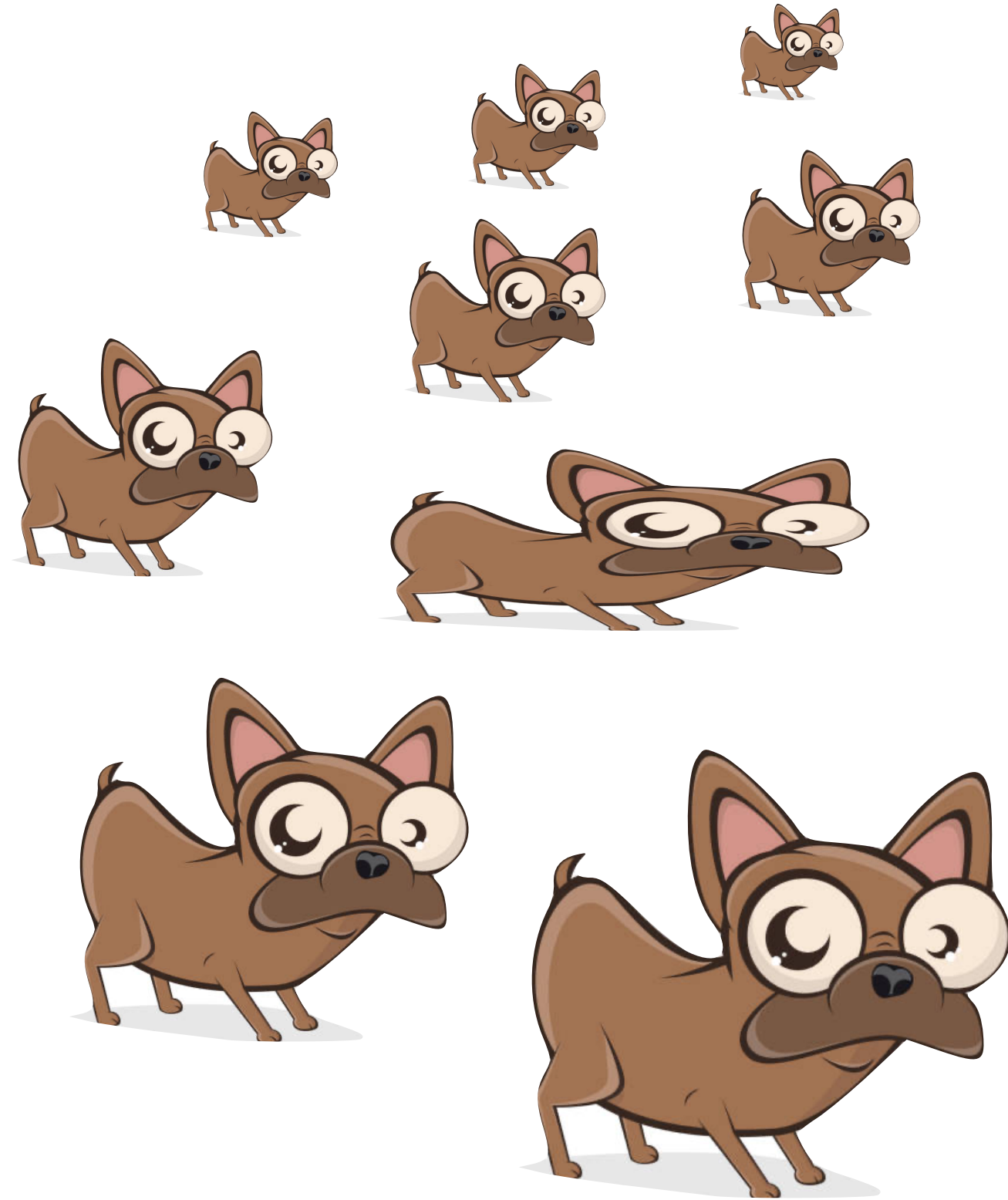
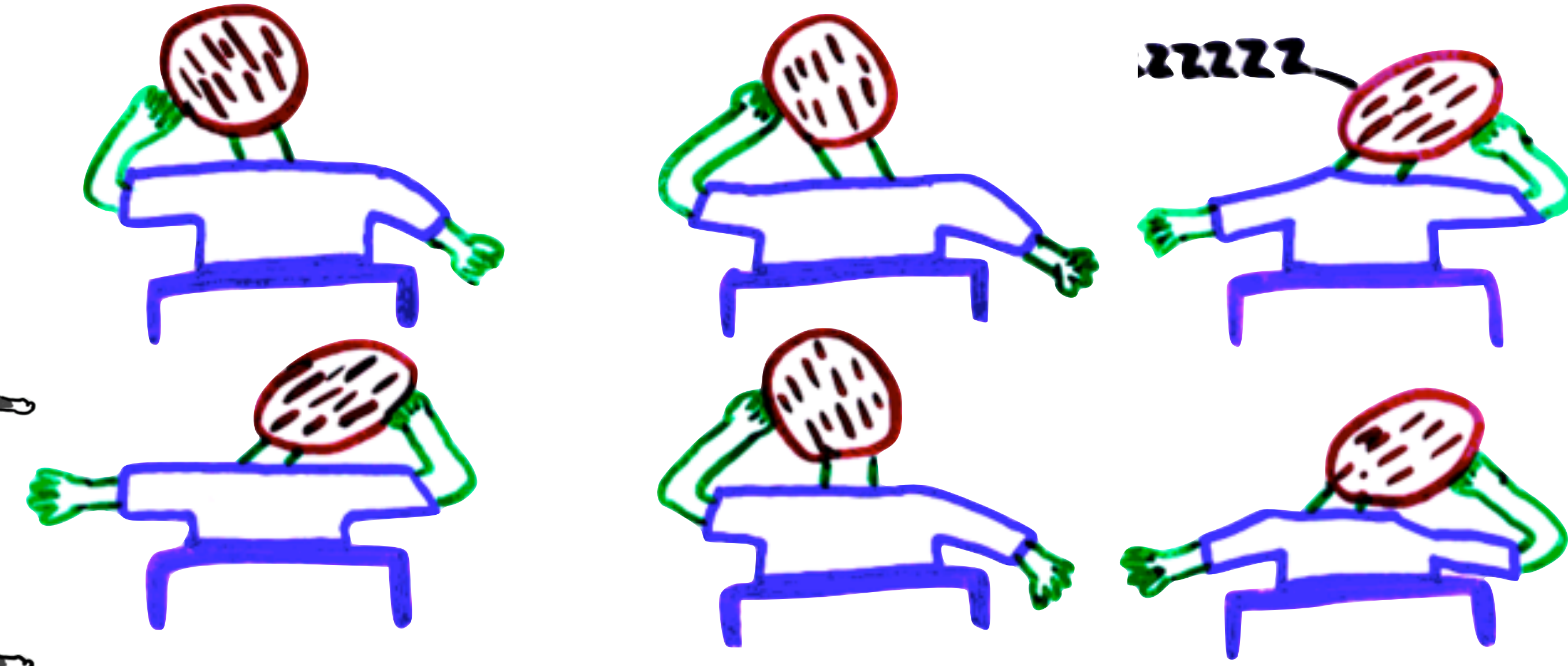
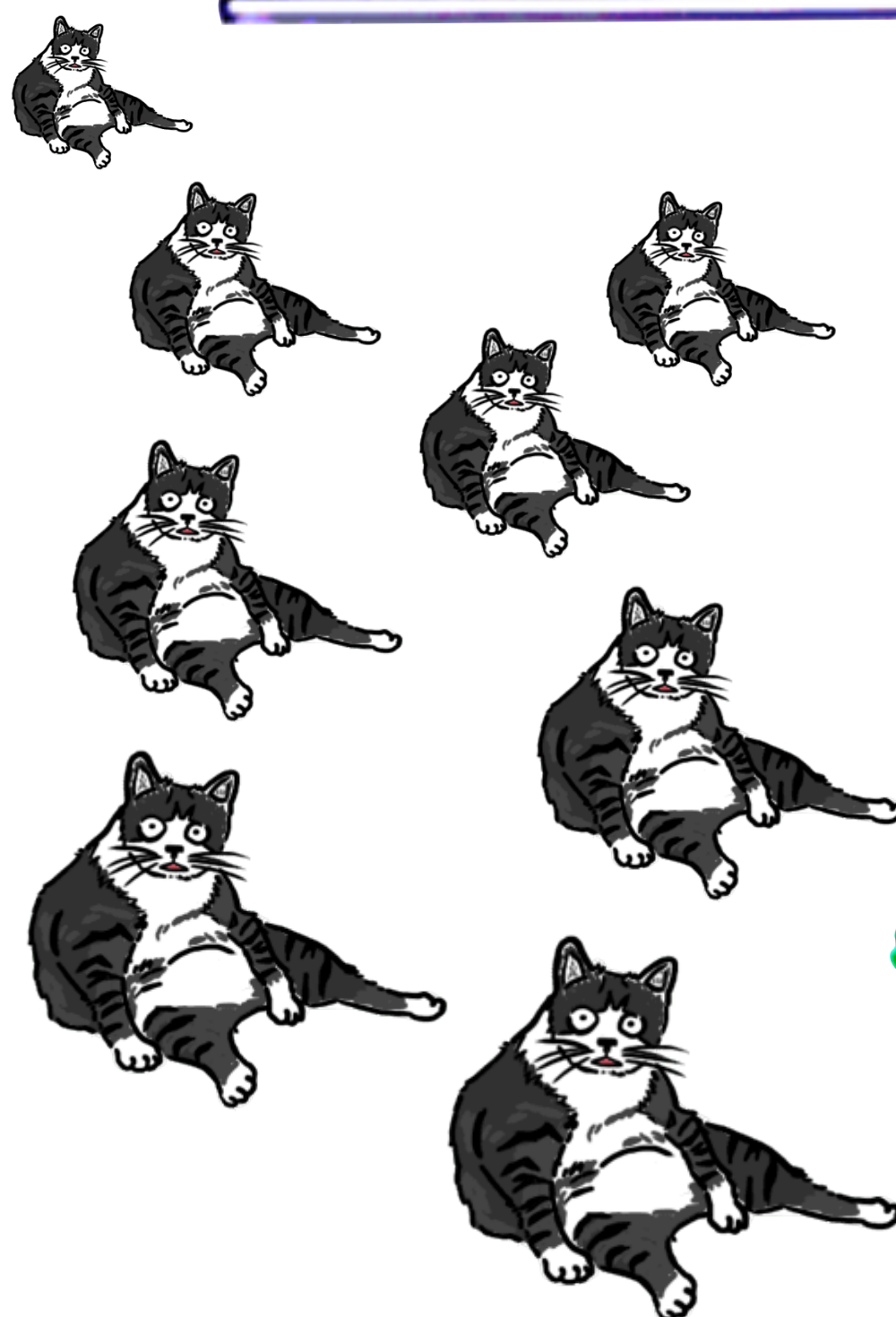
Enoch Peserico
U Padova

Michele Scquizzato
U Padova

Parallel paging is a different animal from sequential paging.

Let's talk about the challenges with *online* parallel paging.

Incongruity: using the phrase "different animal" in this workshop.



Challenge 1: how to partition the cache among the threads?

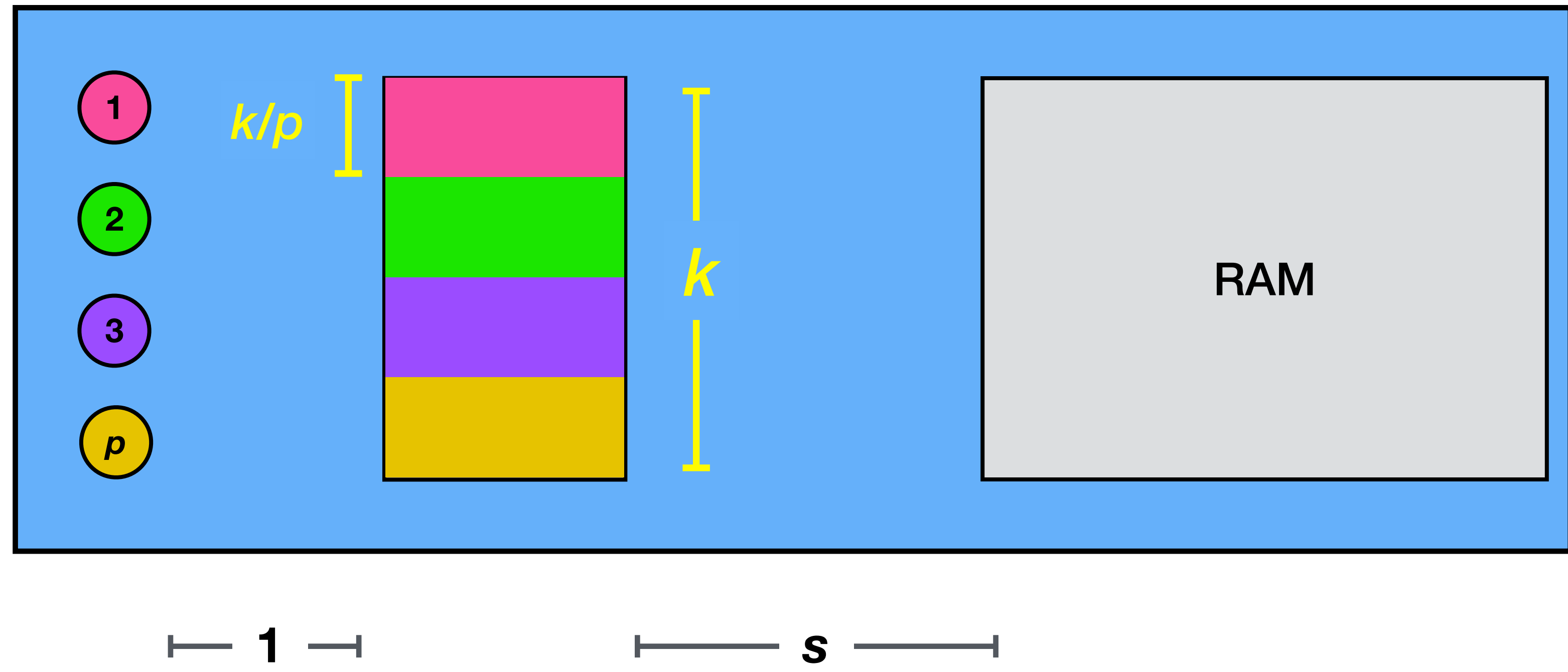
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Challenge 1: how to partition the cache among the threads?

p threads

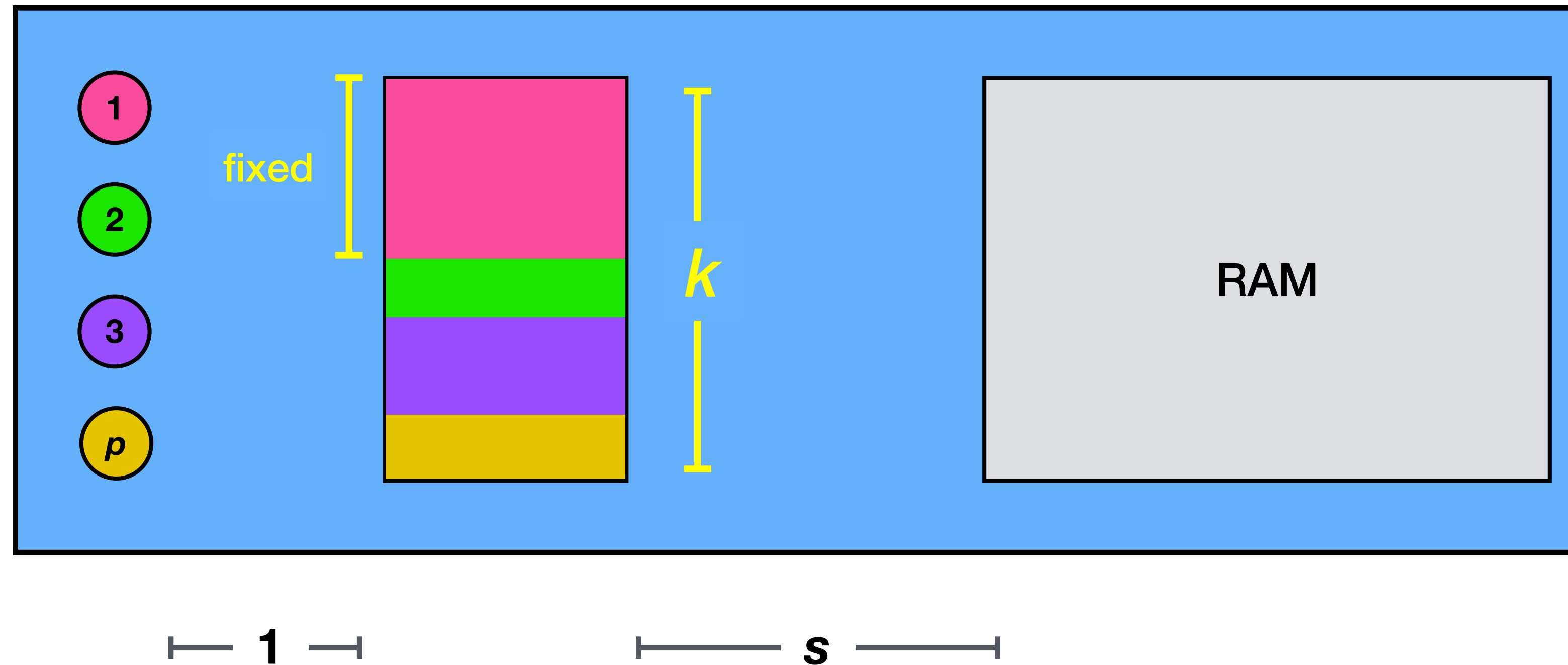
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge 1: how to partition the cache among the threads?

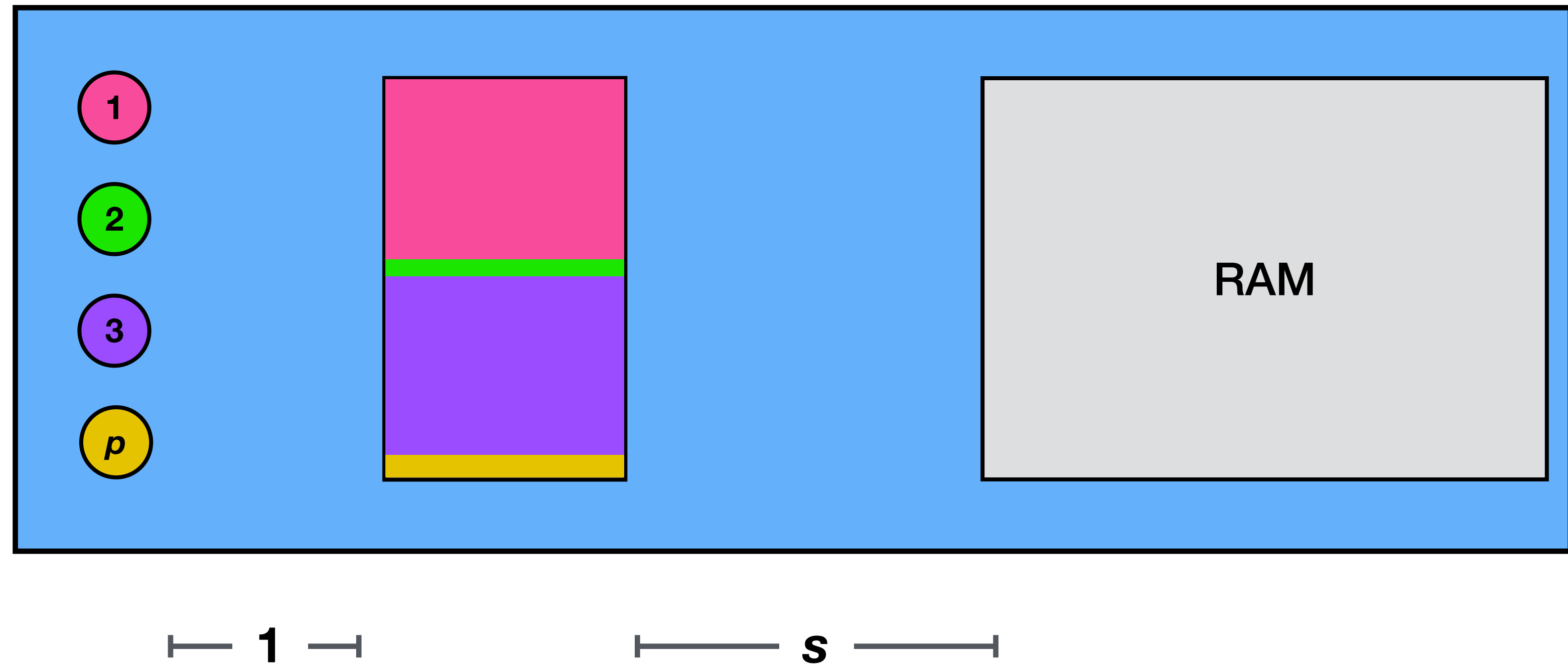
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

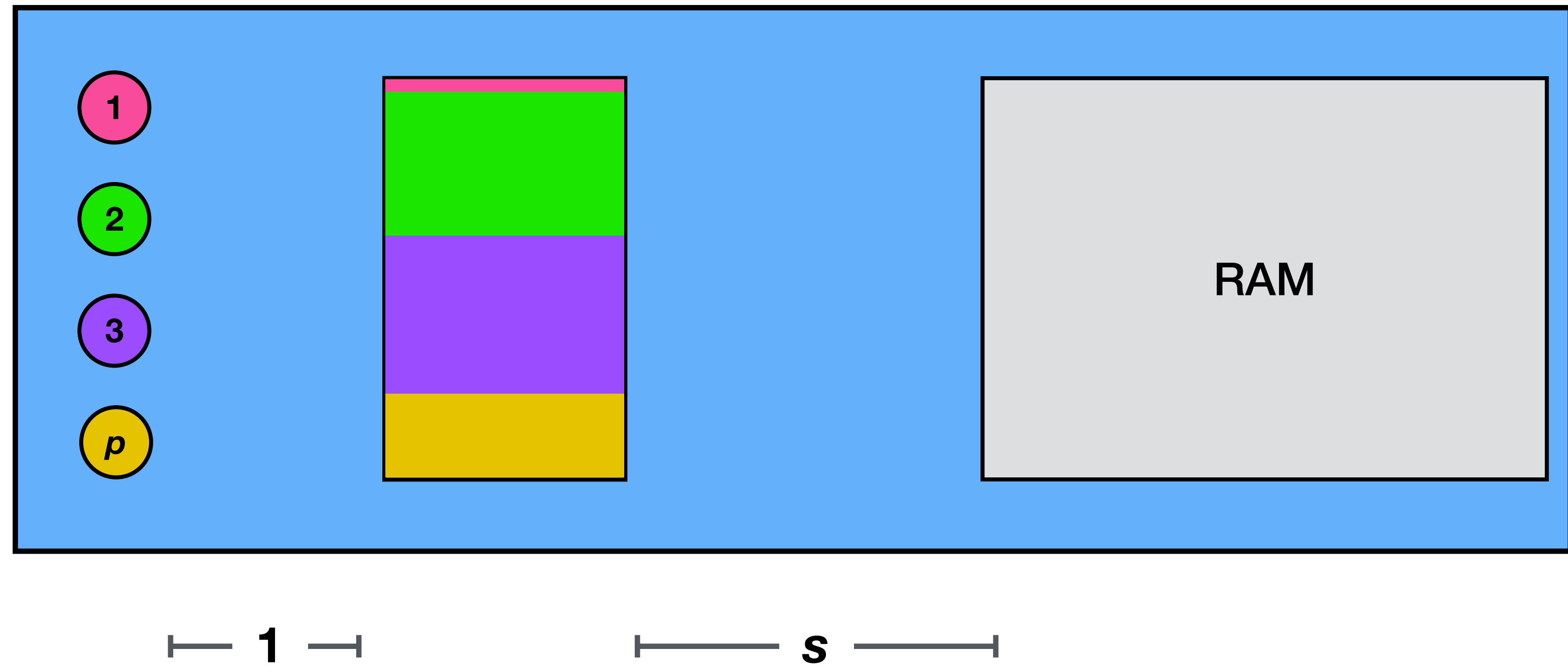
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

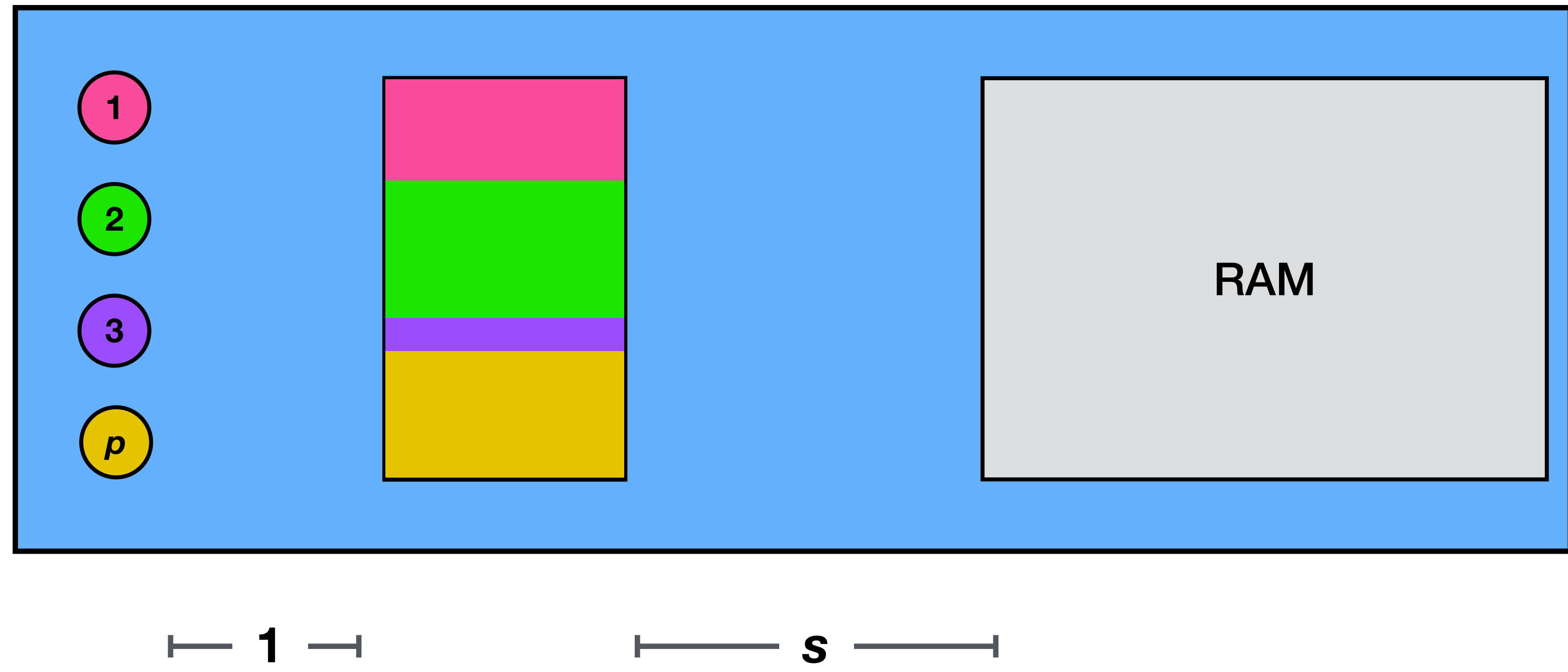
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

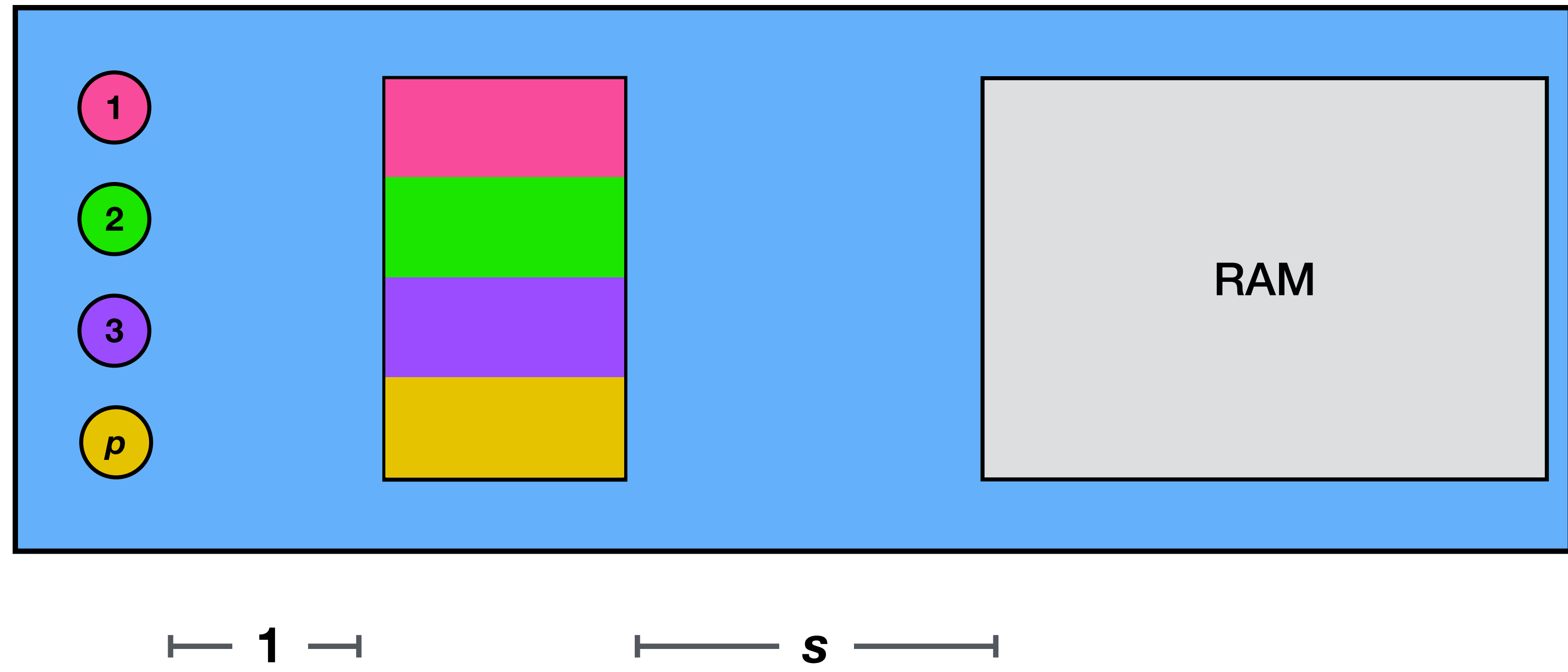
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

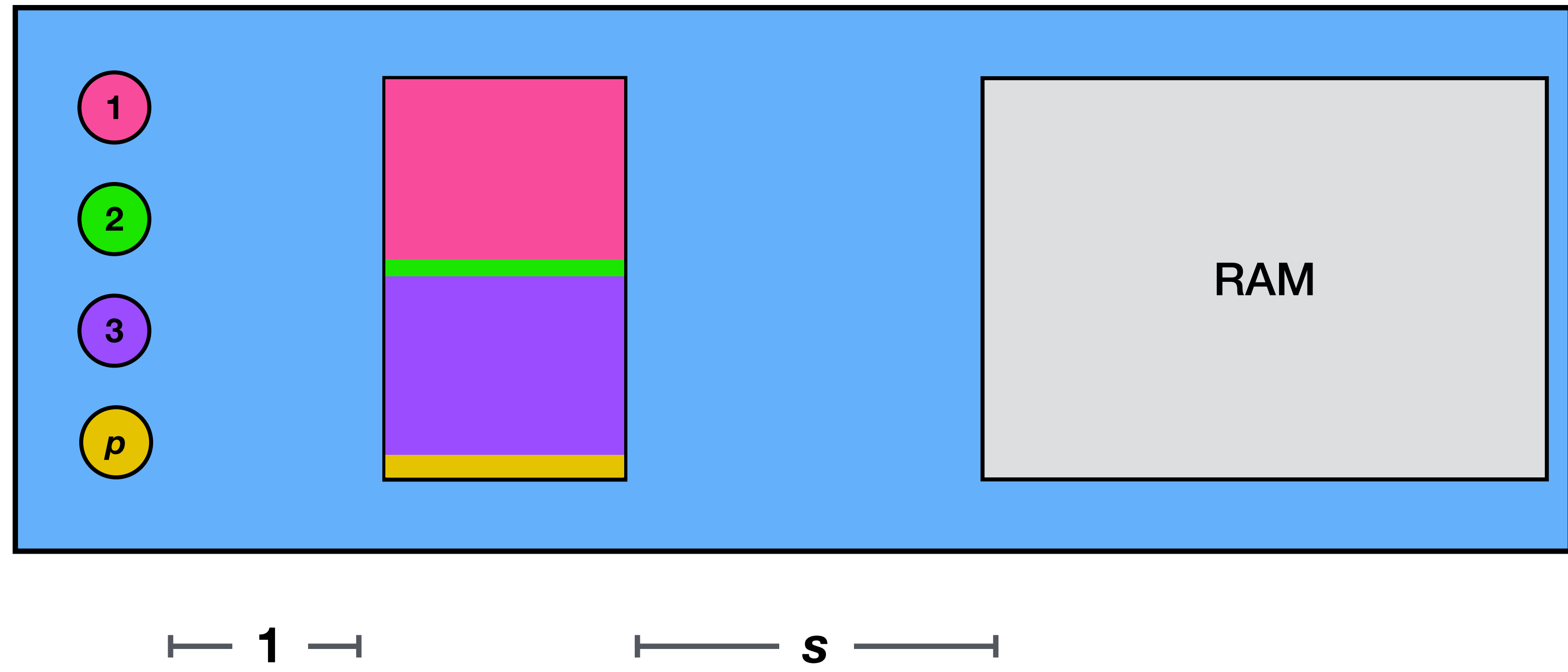
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

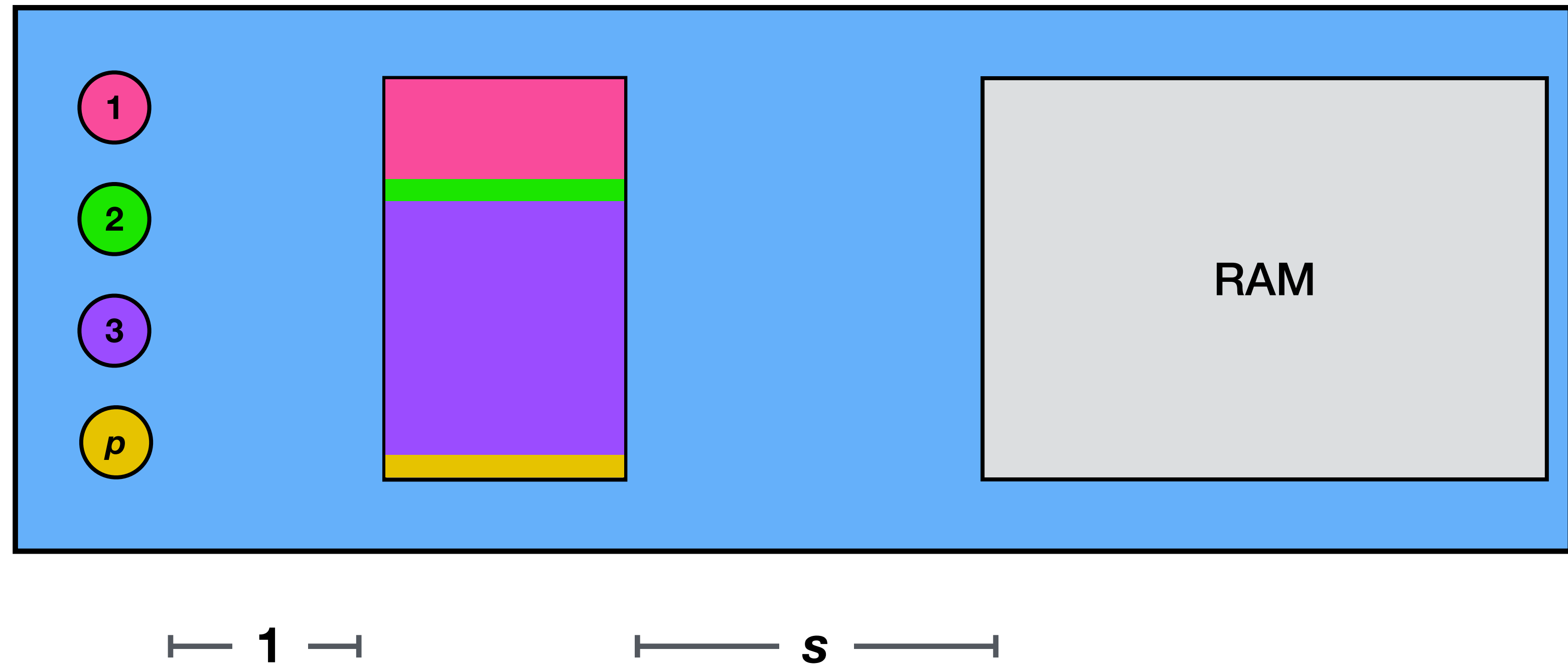
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

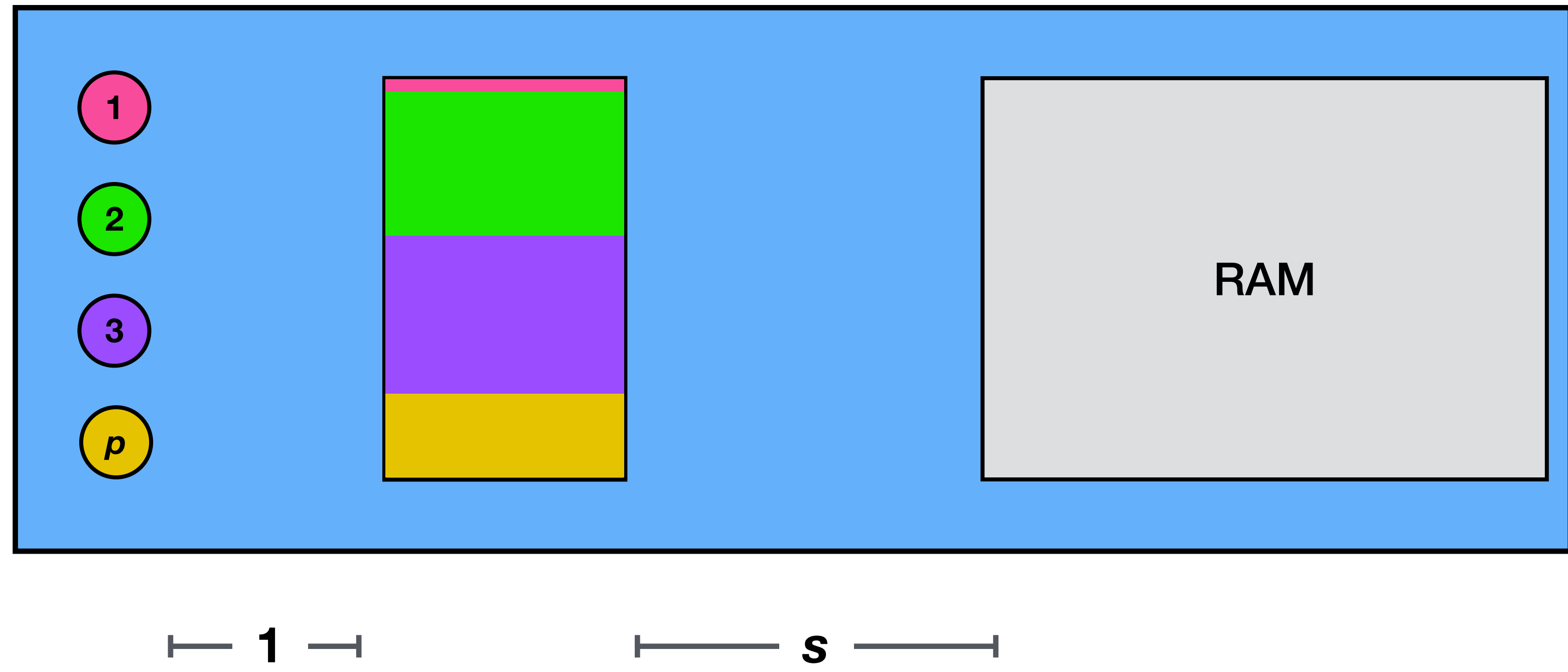
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

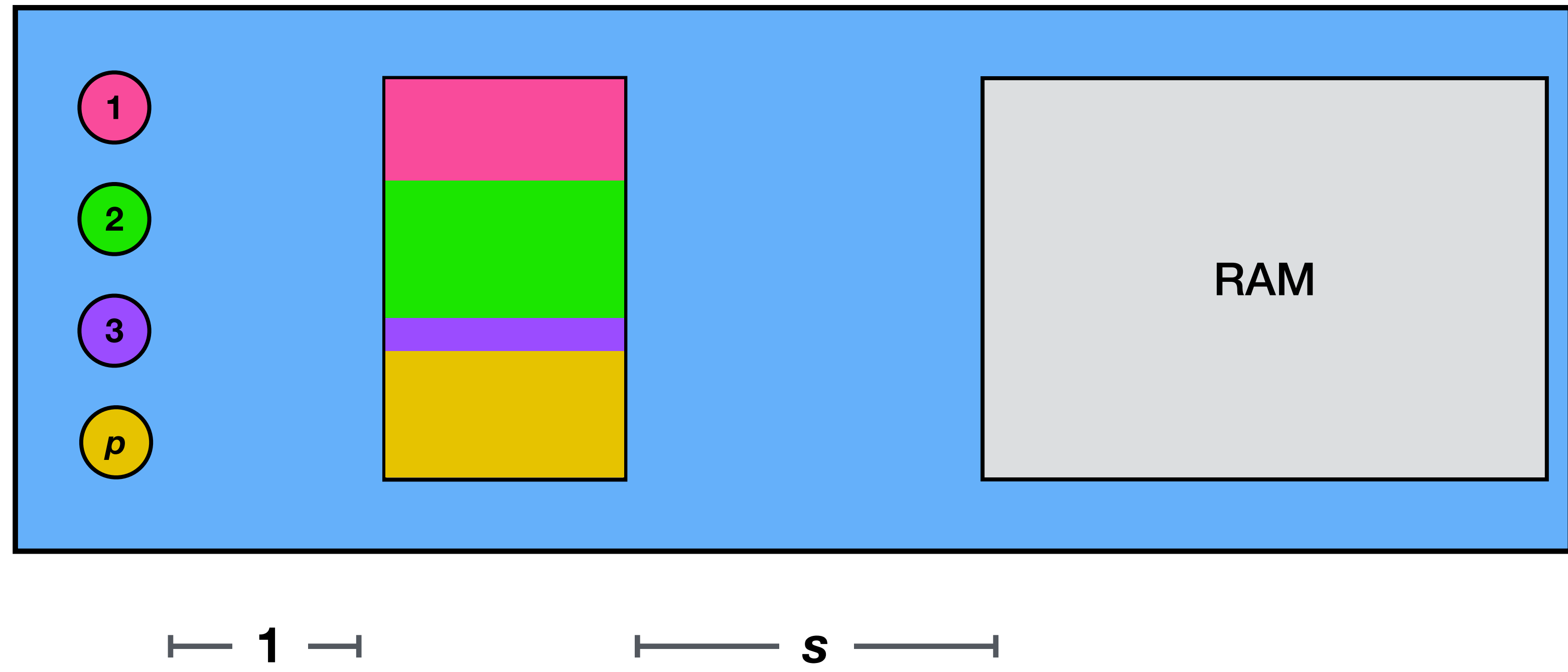
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

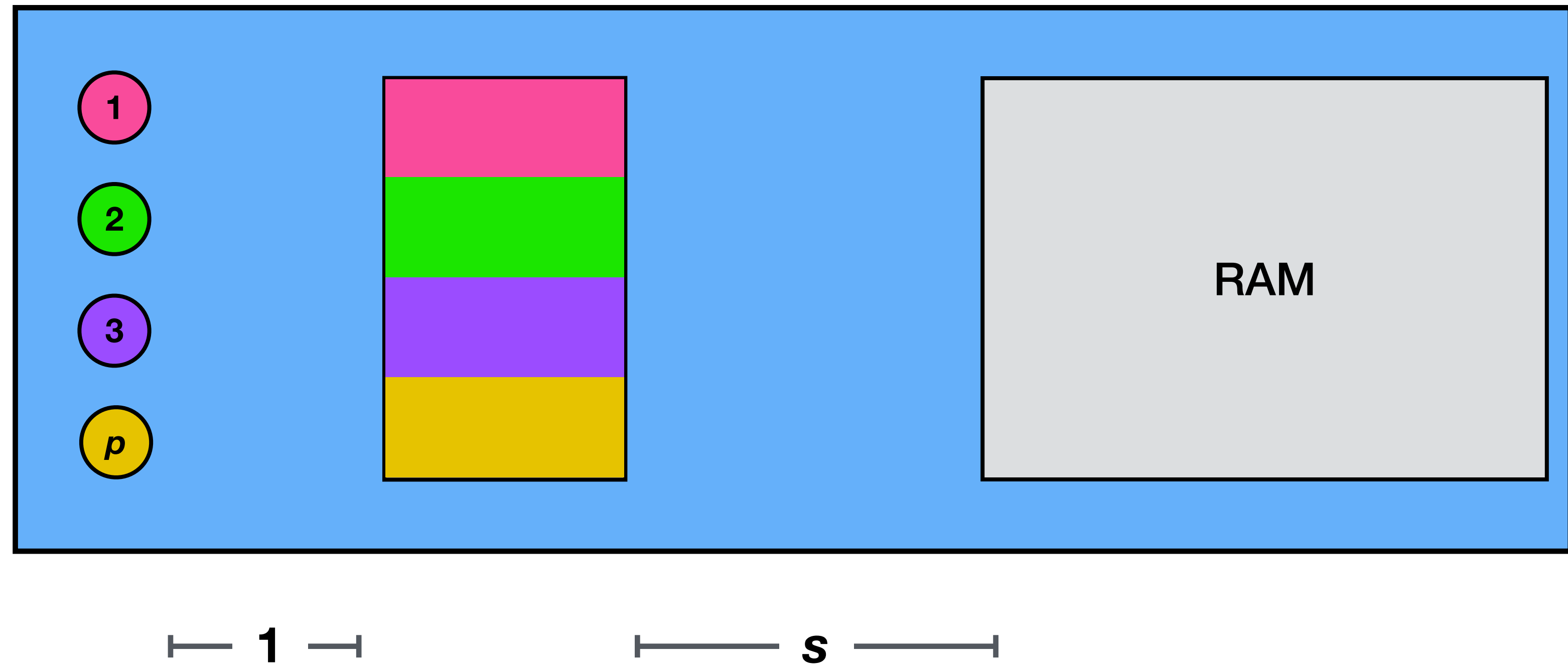
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

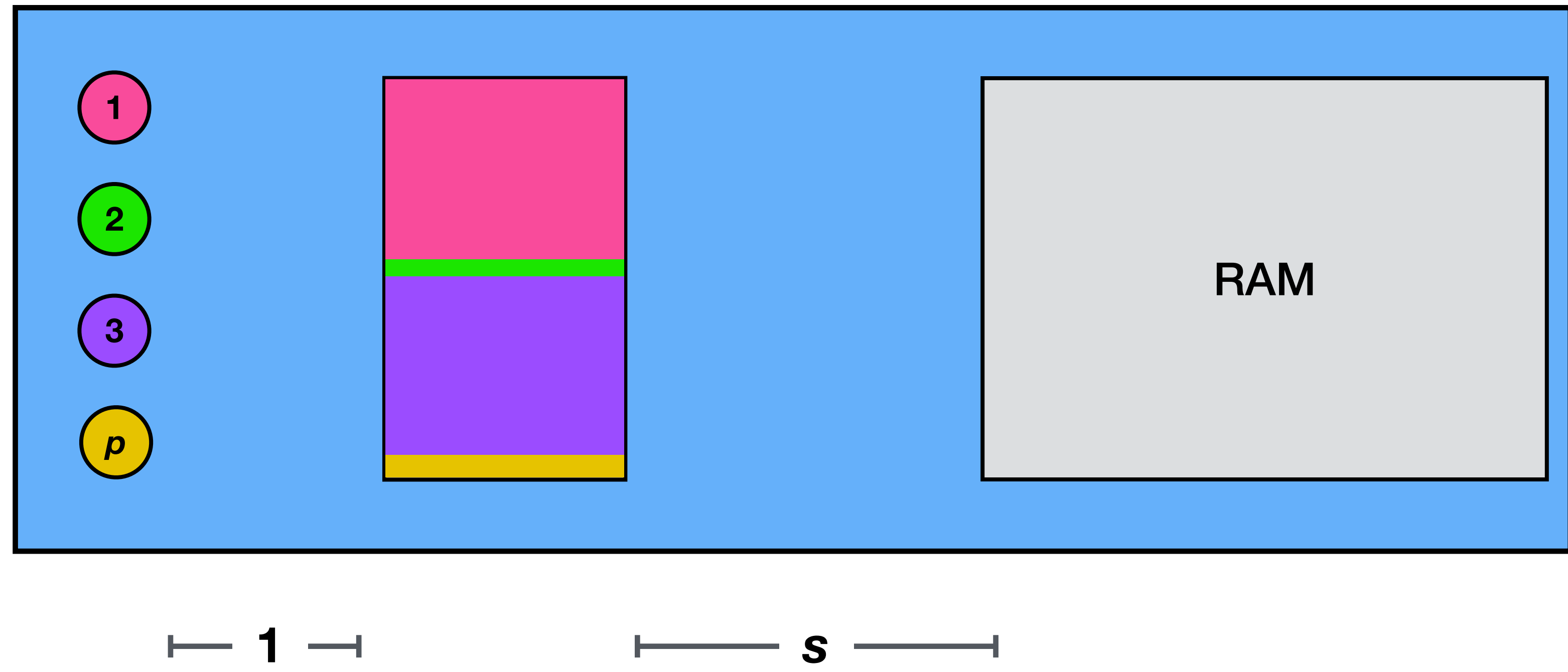
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 1: how to partition the cache among the threads?

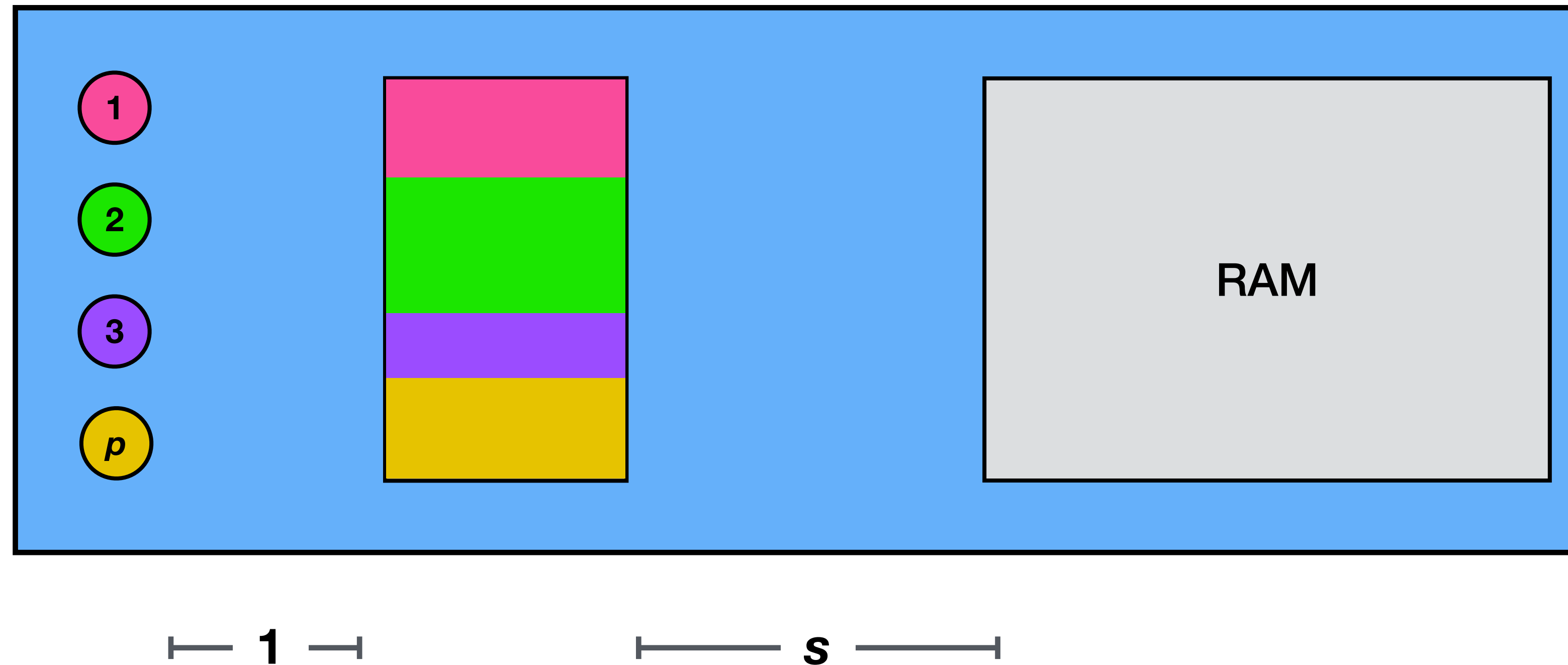
p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$

$r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$

$r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$

$r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



Dividing the cache evenly among the threads is bad: $\Omega(s \cdot \text{OPT})$.

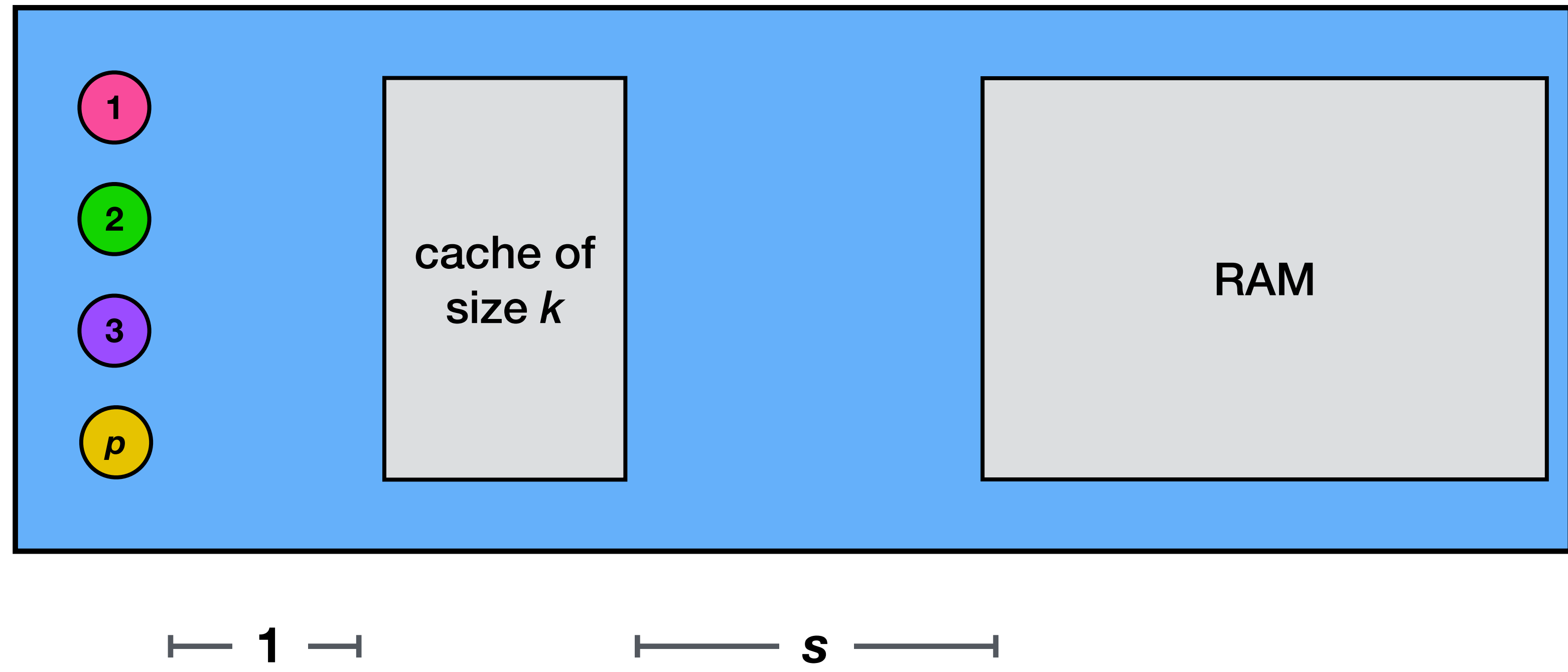
Any fixed allocation is similarly bad.

Challenge: how to *dynamically* partition the cache among the threads?

Challenge 2: how to interleave/schedule the individual threads?

p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$
 $r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$
 $r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$
 $r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$

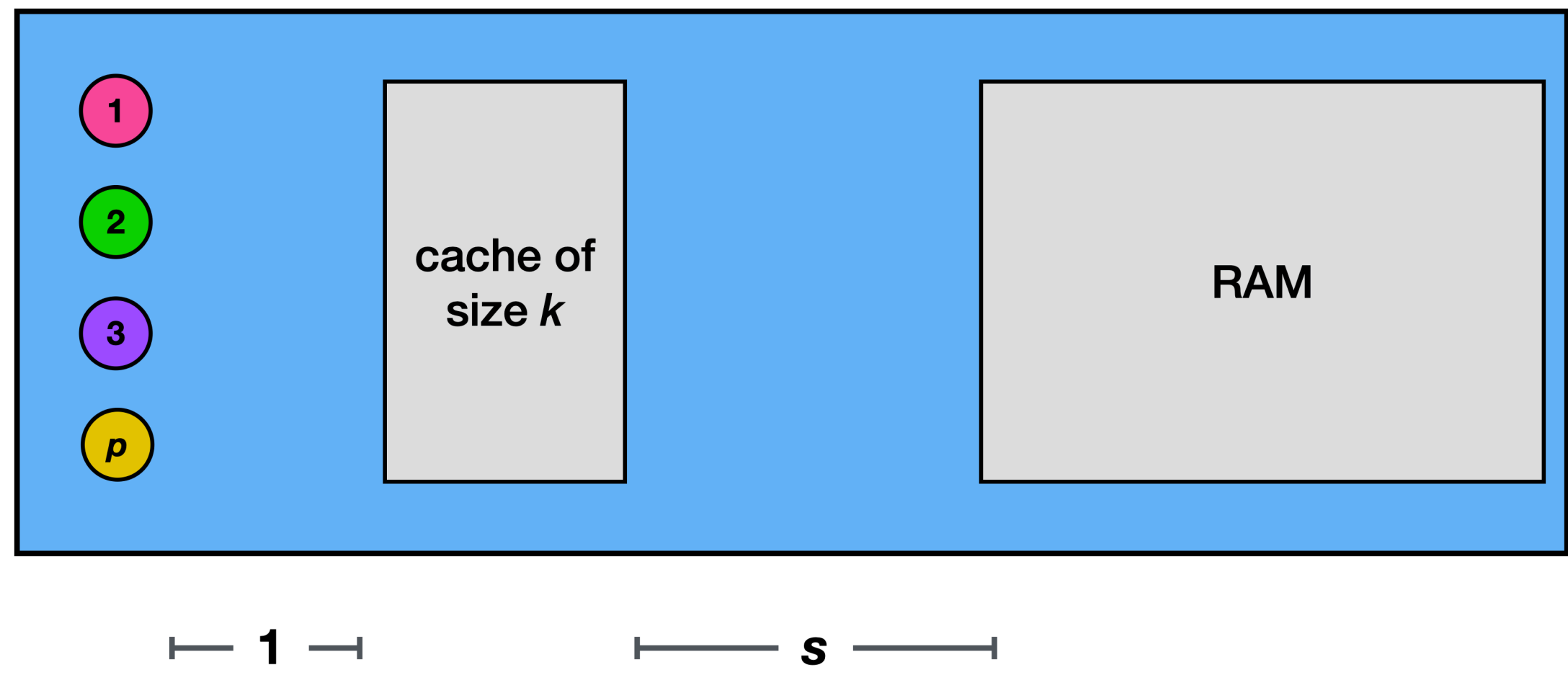


Our decisions cause processors to move at different speeds.
In general, threads cannot be scheduled in lock-step.

Challenge 2: how to interleave/schedule the individual threads?

p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$
 $r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$
 $r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$
 $r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



In general, threads cannot be scheduled in lock-step.

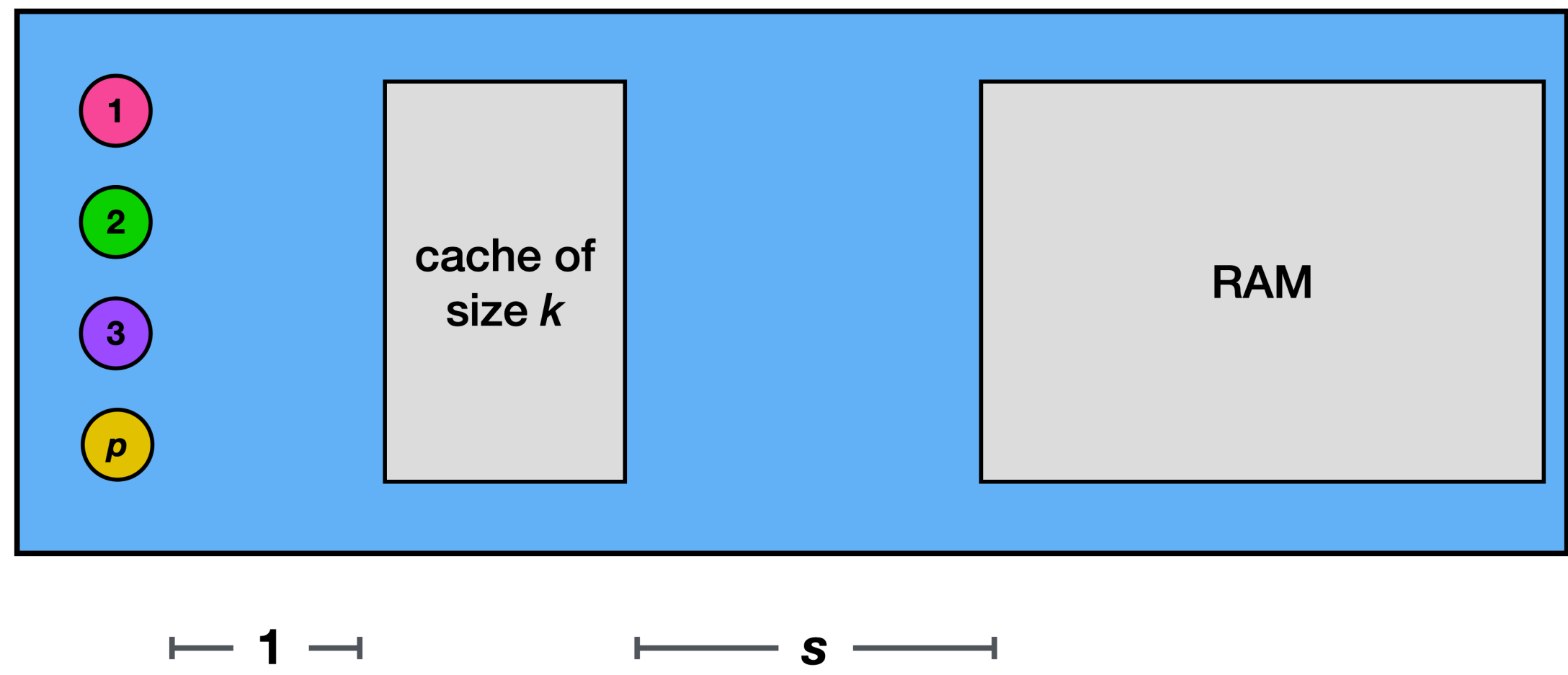
Ex:

- p_1 accesses pages (round robin) $\in [0, k/2)$
- p_2 " $\in [k/2, k)$
- p_3 " $\in [k, 3k/2)$
- p_4 " $\in [3k/2, 2k)$

Challenge 2: how to interleave/schedule the individual threads?

p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$
 $r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$
 $r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$
 $r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$

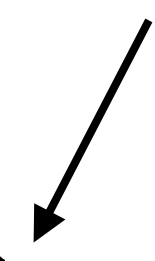


In general, threads cannot be scheduled in lock-step.

Ex:

- p_1 accesses pages (round robin) $\in [0, k/2)$
- p_2 " $\in [k/2, k)$
- p_3 " $\in [k, 3k/2)$
- p_4 " $\in [3k/2, 2k)$

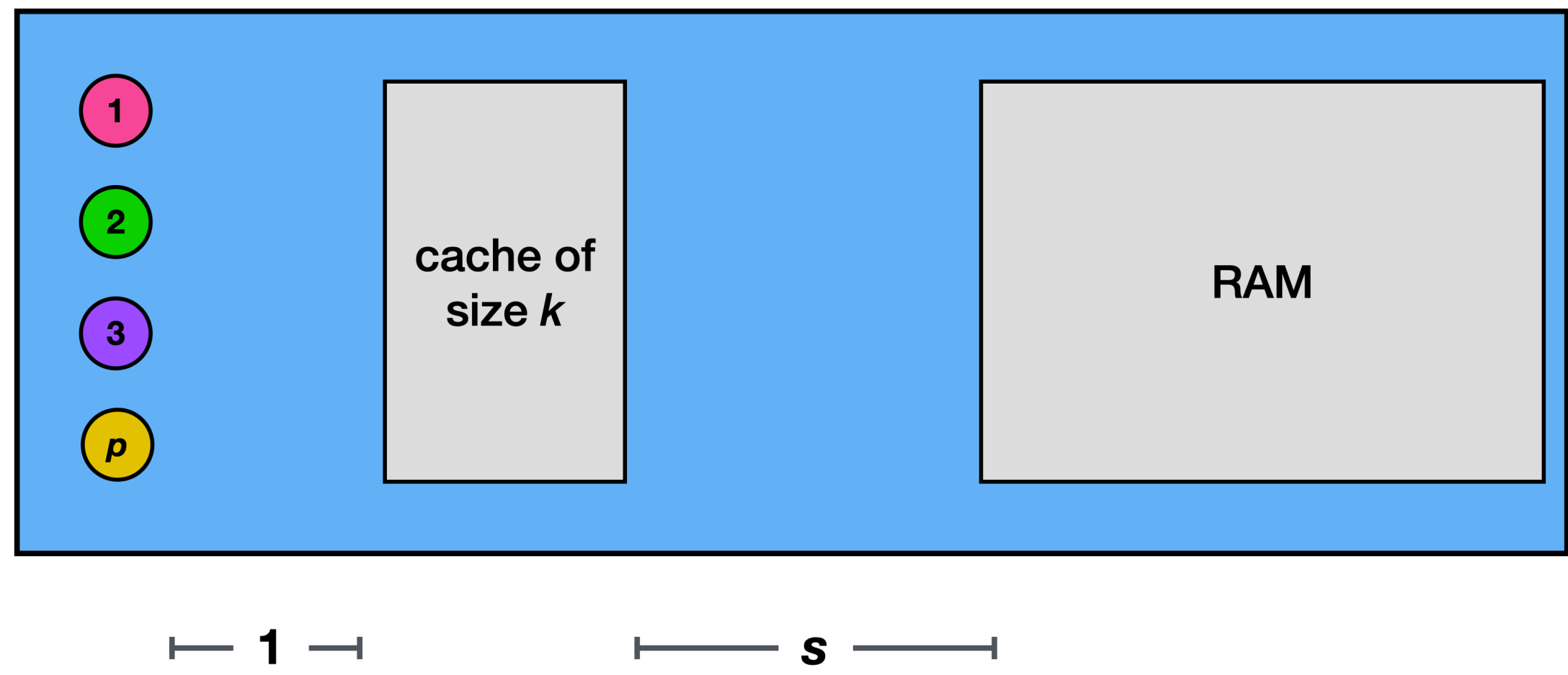
working sets
of 2 threads
fit in cache,
but not 3



Challenge 2: how to interleave/schedule the individual threads?

p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$
 $r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$
 $r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$
 $r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



In general, threads cannot be scheduled in lock-step.

Ex:

- p_1 accesses pages (round robin) $\in [0, k/2)$
- p_2 " $\in [k/2, k)$
- p_3 " $\in [k, 3k/2)$
- p_4 " $\in [3k/2, 2k)$

working sets
of 2 threads
fit in cache,
but not 3

OPT:

- Run p_1 and p_2 to completion.
- Then run p_3 and p_4 to completion.

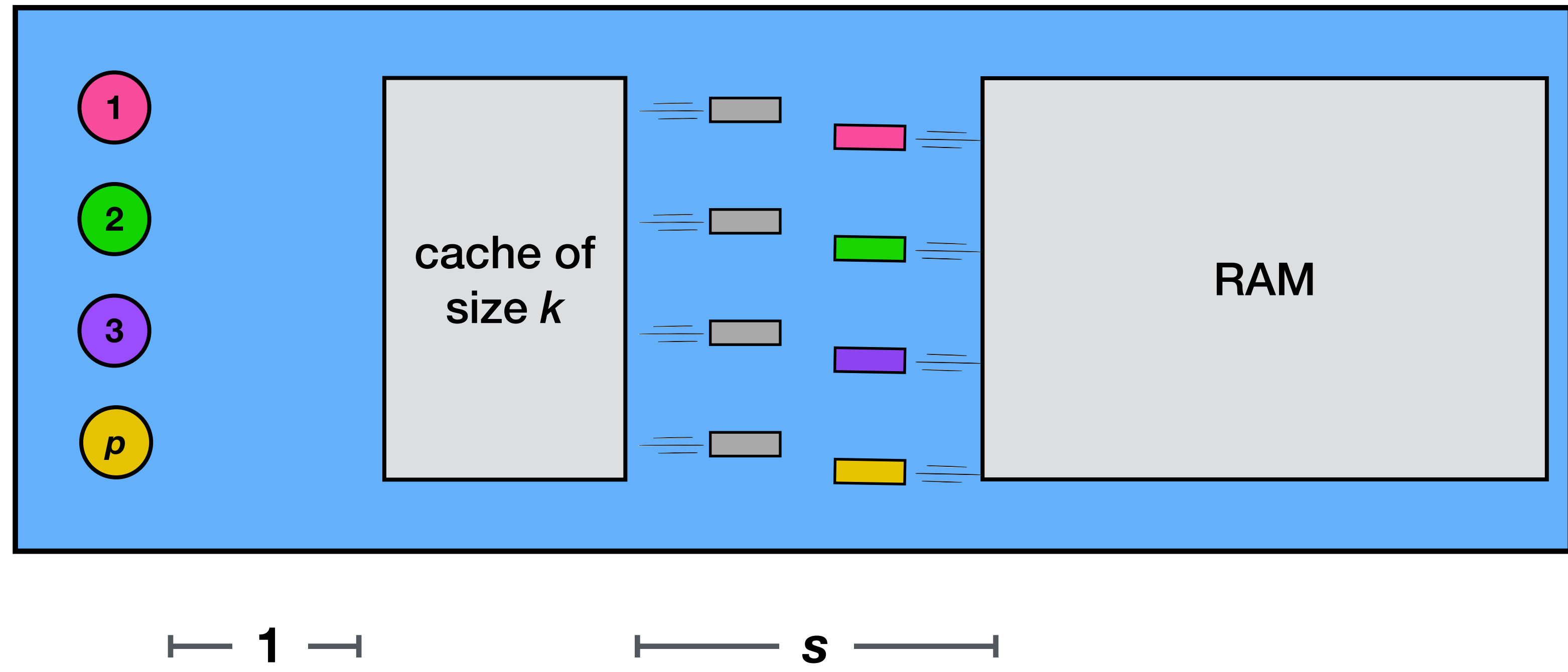
Lock step:

- Makespan is $\Omega(s \cdot \text{OPT})$.

Non-Challenge: What Eviction Policy Should Each Processor Use?

p threads
threads access (disjoint) blocks

$r_{11}, r_{12}, r_{13}, r_{14}, r_{15}, r_{16}, \dots$
 $r_{21}, r_{22}, r_{23}, r_{24}, r_{25}, r_{26}, \dots$
 $r_{31}, r_{32}, r_{33}, r_{34}, r_{35}, r_{36}, \dots$
 $r_{p1}, r_{p2}, r_{p3}, r_{p4}, r_{p5}, r_{p6}, \dots$



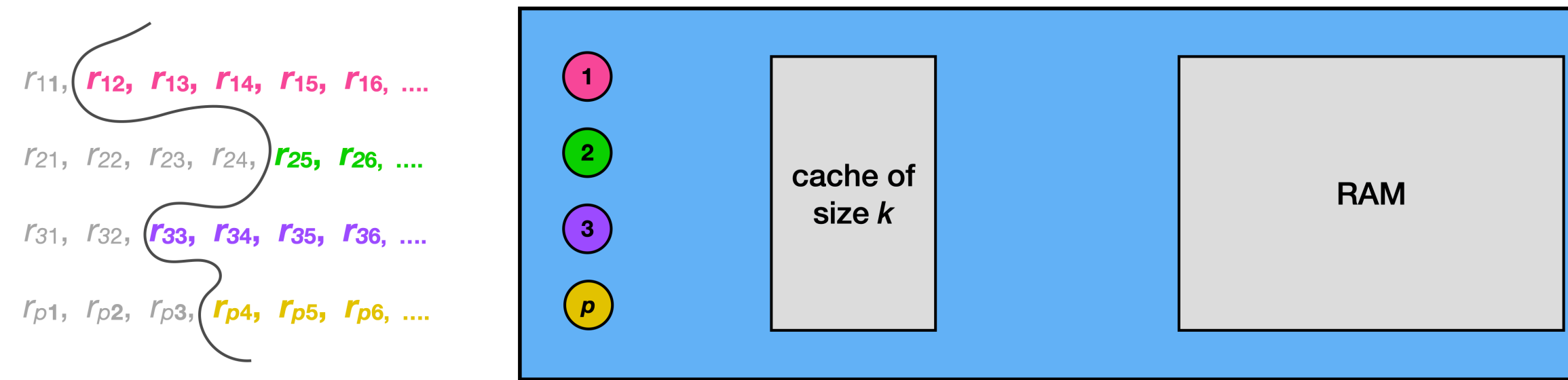
Question: What are the eviction policies of individual threads, given that a thread's allotment of cache changes over time?

Answer: Each processor should still just use LRU.

[Bender, Ebrahimi, Fineman, Ghasemiefteh, Johnson, McCauley SODA 2014]

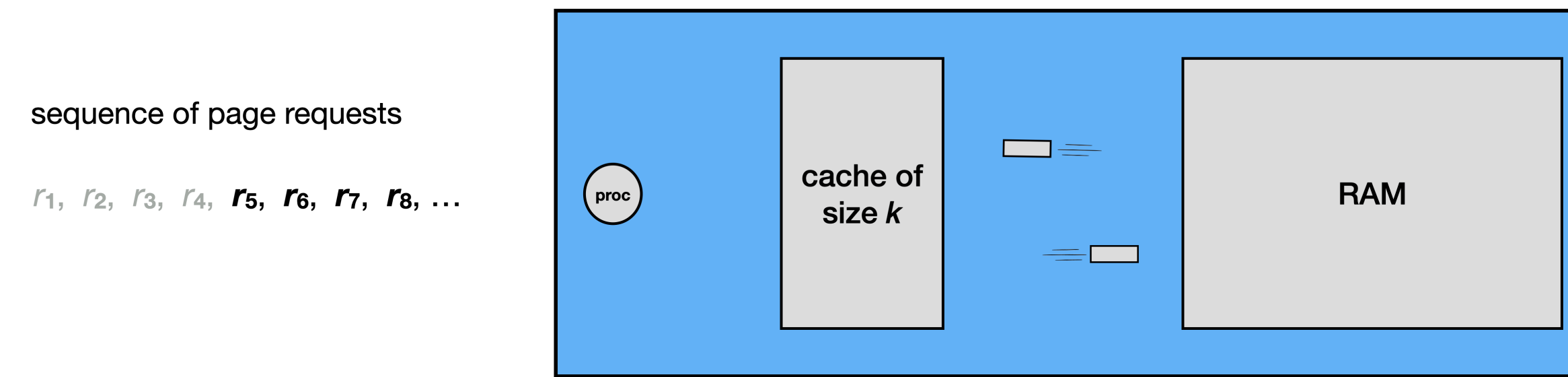
Summary of parallel-paging challenges

Parallel paging



- How to partition the cache among the threads?
- How to interleave/schedule the individual threads?
- What are the eviction policies of each individual threads?

Sequential paging



- The (single) thread gets all of cache.
- There is a total order in which all page requests are serviced.
- There is a single eviction policy.

Previous results are for the offline problem.

- NP-hardness
- Existing *offline* algs w/ run times exponential in # procs p and cache size k

[Hassidim ICS 2010]

[López-Ortiz & Salinger ITCS 2012
& WAOA 2012]

The online problem has been open since 1995.

[Fiat and Karlin, STOC 1995]

This Talk: $O(\log p)$ -competitive algs for parallel paging

Deterministic online parallel paging algorithm that is

- $O(\log p)$ competitive for *average completion time + makespan* with
- $O(1)$ resource augmentation.

No deterministic online algorithm can do better.

[Agrawal, Bender, Das, Kuszmaul, Peserico, Scquizzato SODA 2021]

[Agrawal, Bender, Das, Kuszmaul, Peserico, Scquizzato SPAA 2022]

Cache as a scarce resource

The cache is a scarce resource.

Each thread should use as little cache as it can.



Cache as a scarce resource

The cache is a scarce resource.

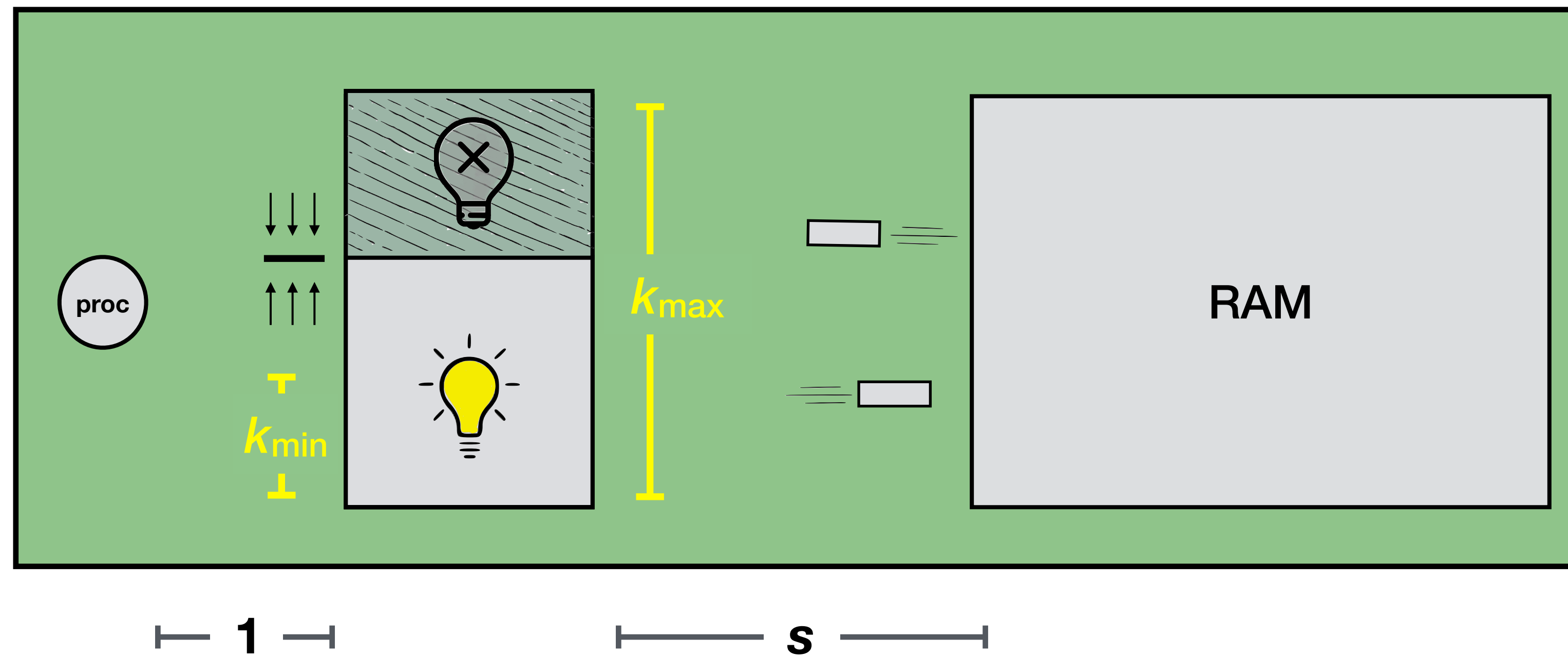
Each thread should use as little cache as it can.



This motivates the very different problem of **green paging.**

The Green Paging Problem

$r_1, r_2, r_3, r_4, r_5, r_6, r_7, r_8, \dots$



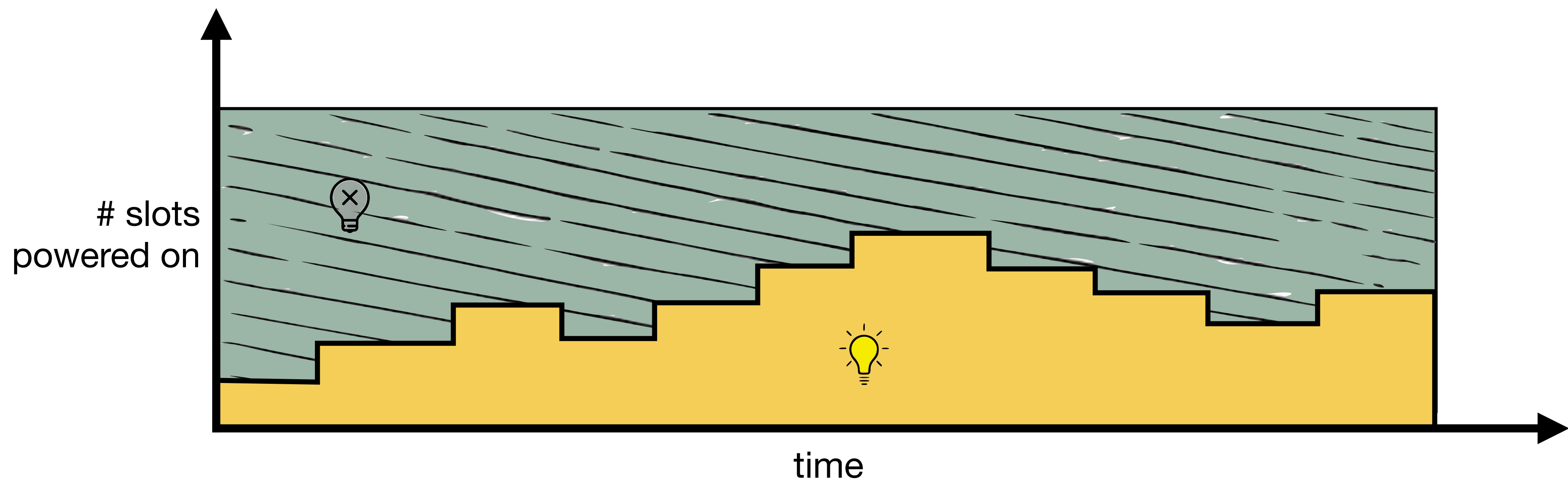
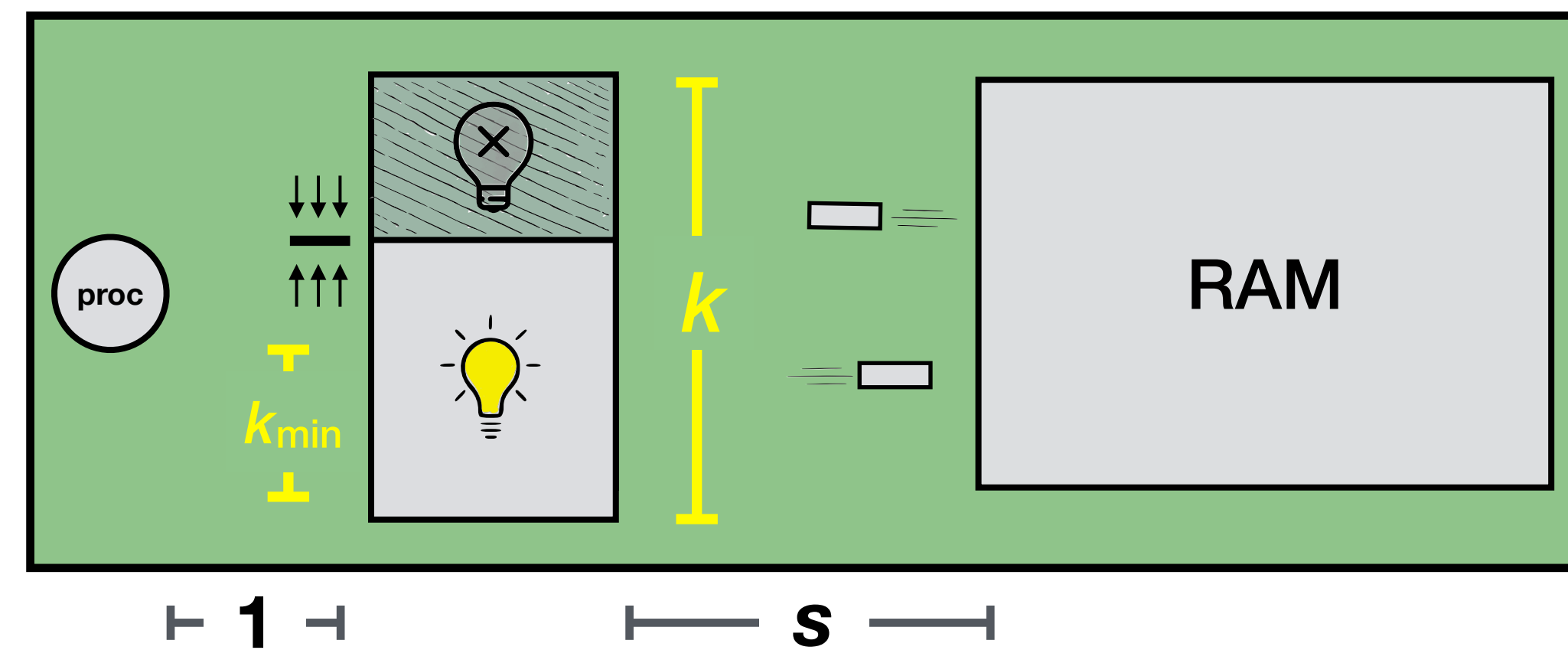
- Single processor.
- Slots in the cache can be *powered off* to save energy.
- Energy consumption in a timestep = Θ (cache slots that are turned on).

Objective: service a request sequence online with minimal energy.

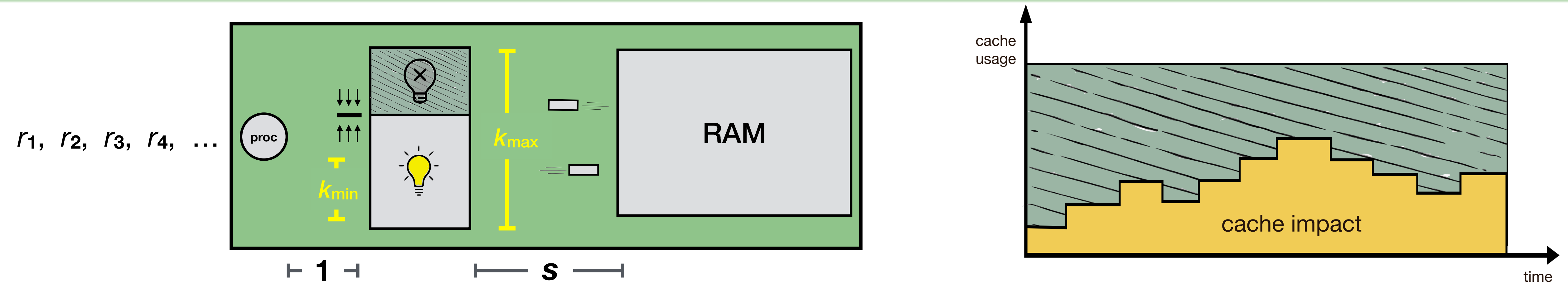


cache impact (“energy”) = $\Theta(\sum_{\text{time } t} \# \text{ cache slots powered on})$

$R = r_1, r_2, r_3, r_4, \dots$



Green paging



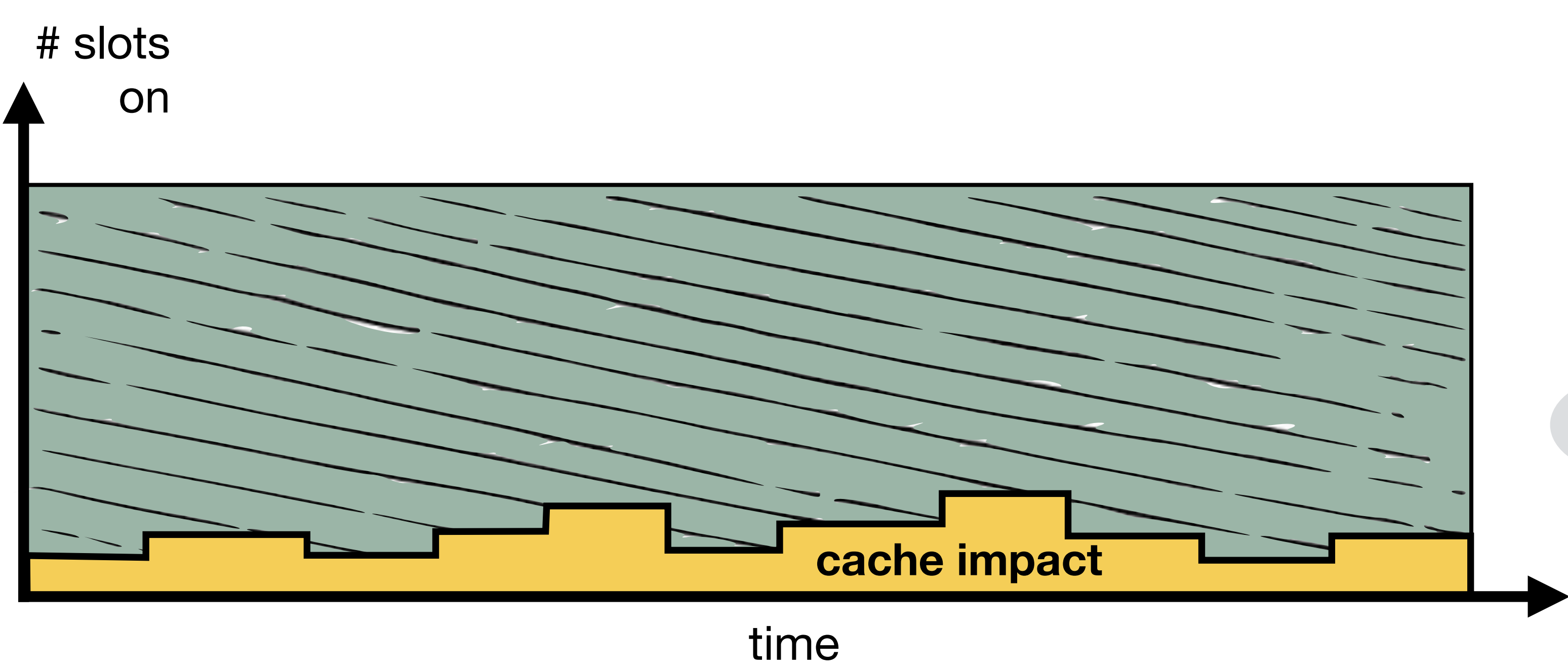
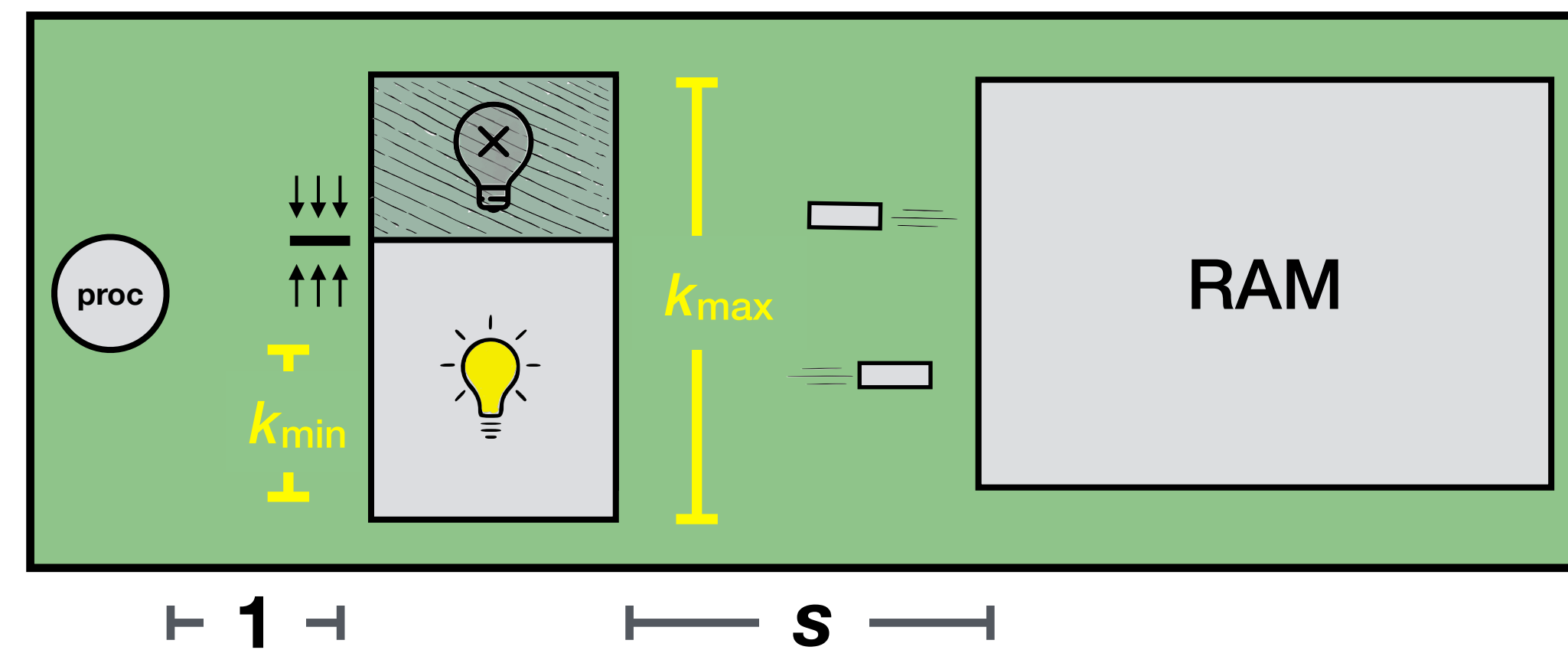
Given a page request sequence $R = r_1, r_2, r_3, \dots$ design

- **cache-space allocation** over time, and
- **page-replacement policy,**

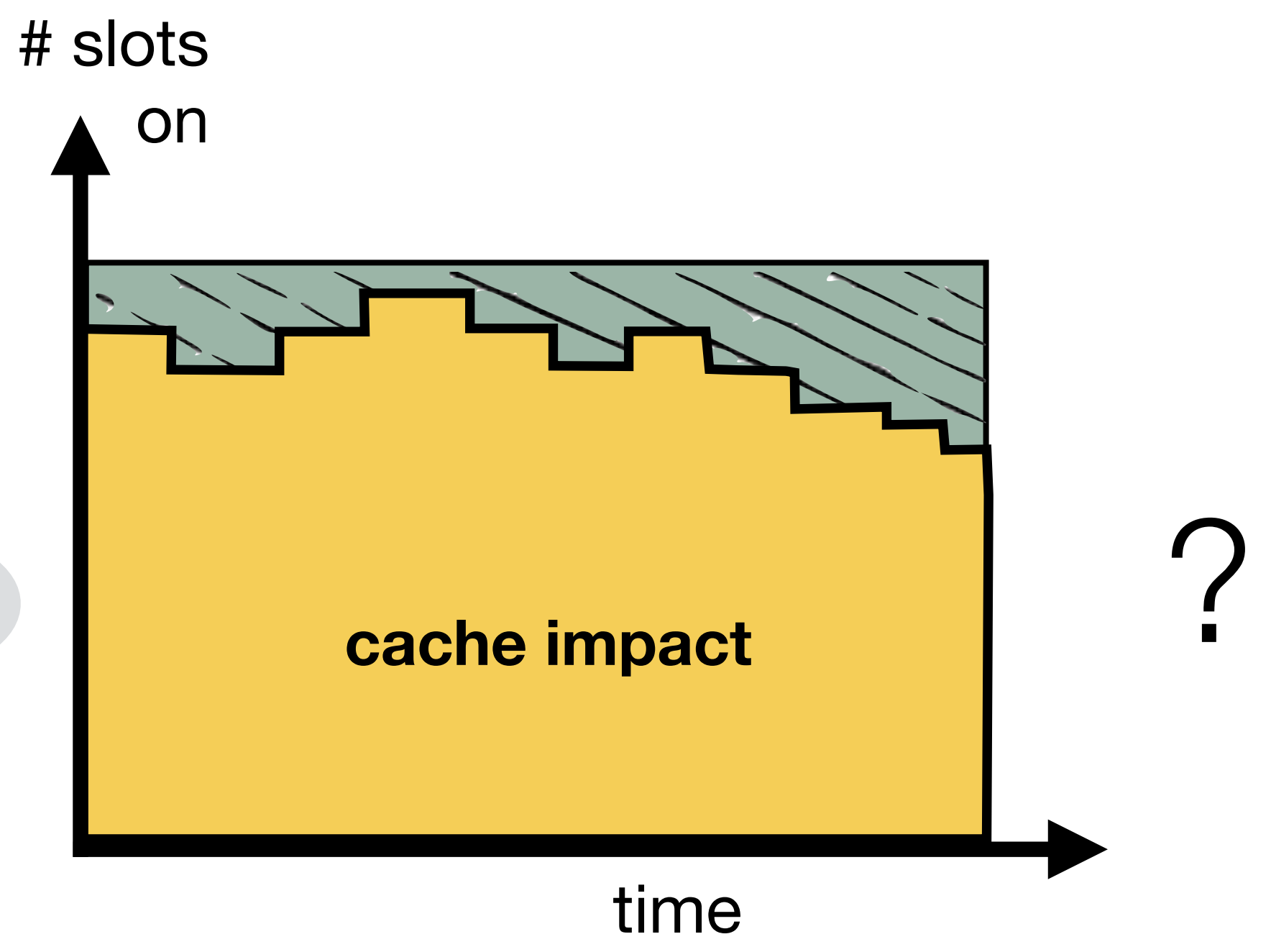
to minimize the cache impact to serve R .

Green paging dilemma

$$R = r_1, r_2, r_3, r_4, \dots$$



versus



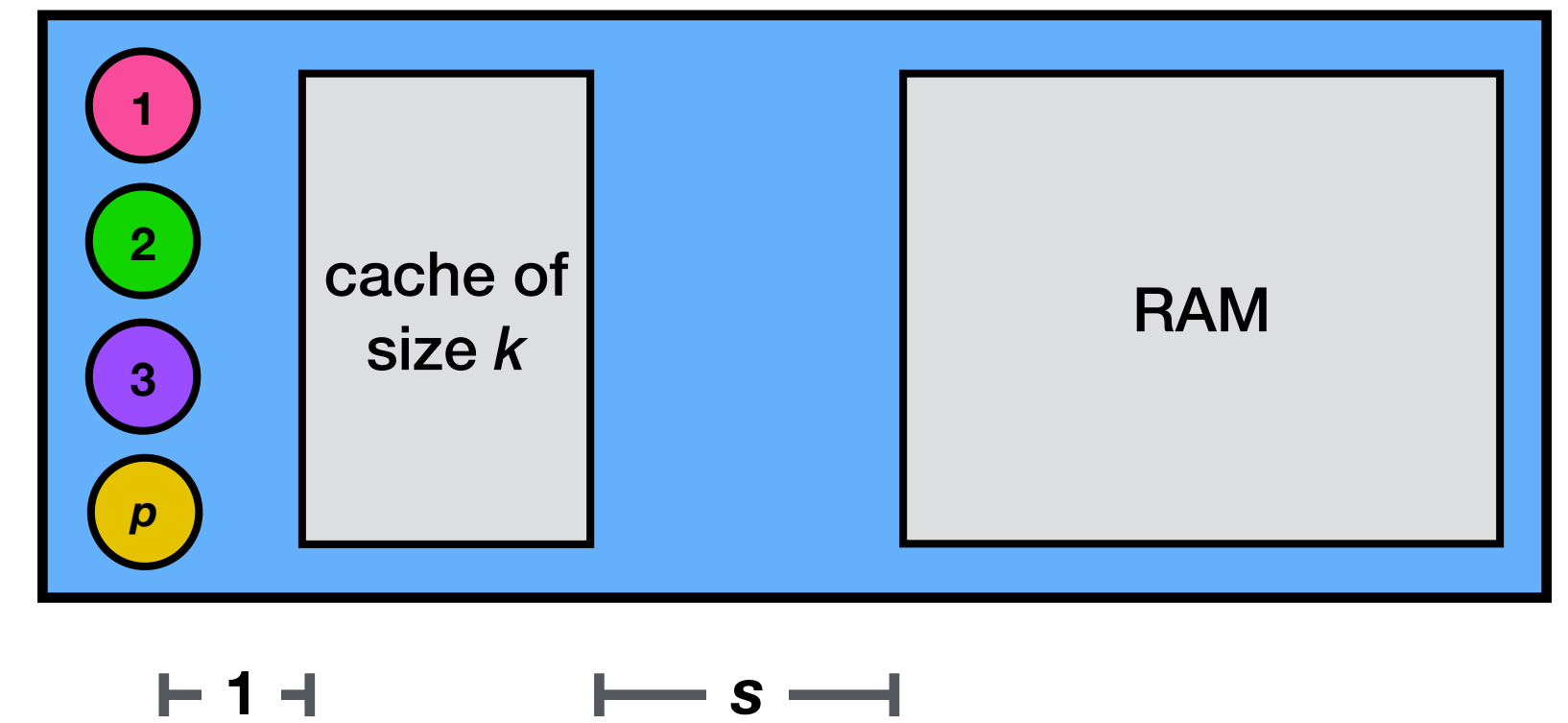
Solve the green-paging problem independently for each thread

$$R_1 = r_{11}, r_{12}, r_{13}, r_{14}, \dots$$

$$R_2 = r_{21}, r_{22}, r_{23}, r_{24}, \dots$$

$$R_3 = r_{31}, r_{32}, r_{33}, r_{34}, \dots$$

$$R_p = r_{p1}, r_{p2}, r_{p3}, r_{p4}, \dots$$



Online green paging \longleftrightarrow Online parallel paging

[Agrawal, Bender, Das, Kuszmaul, Peserico, Scquizzato SODA 2021, SPAA 2022]

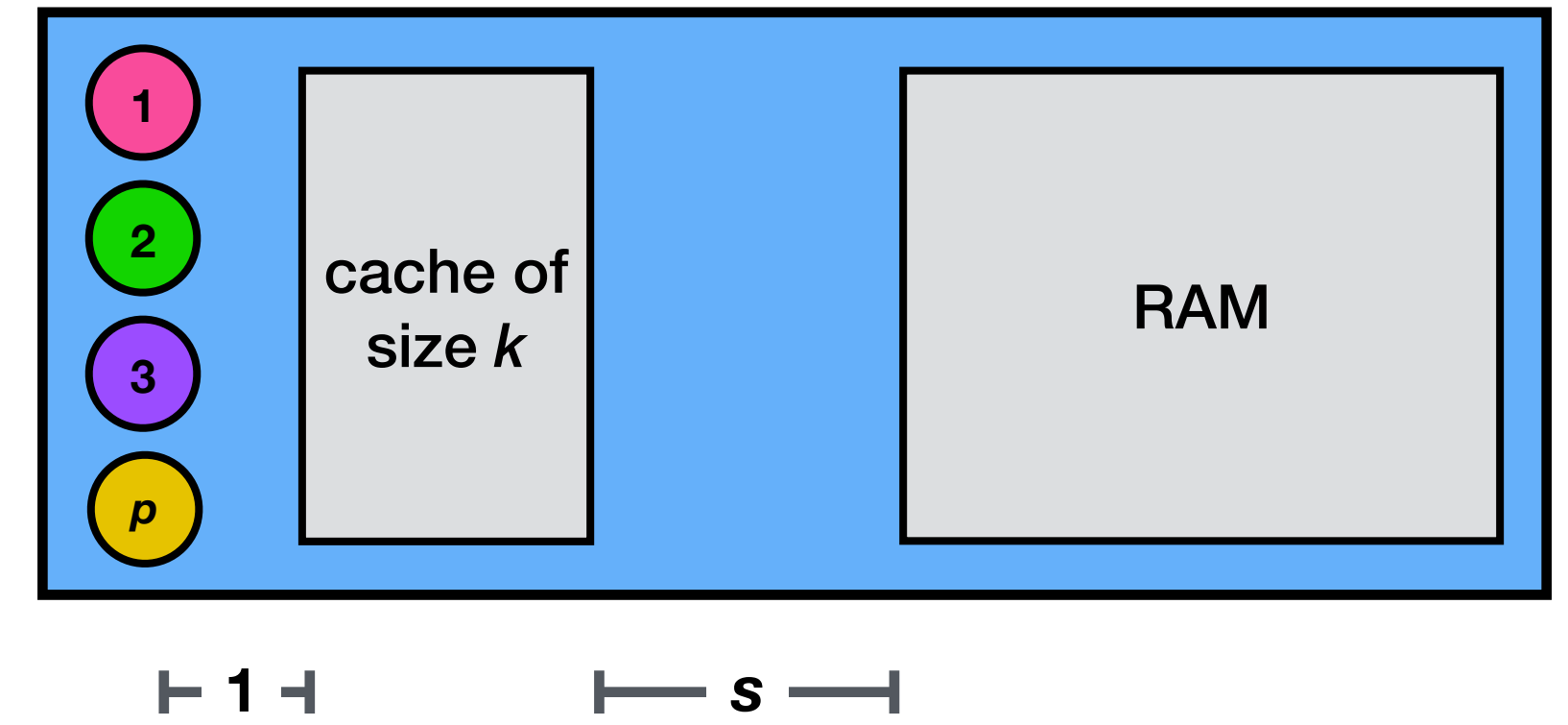
Solve the green-paging problem independently for each thread

$$R_1 = r_{11}, r_{12}, r_{13}, r_{14}, \dots$$

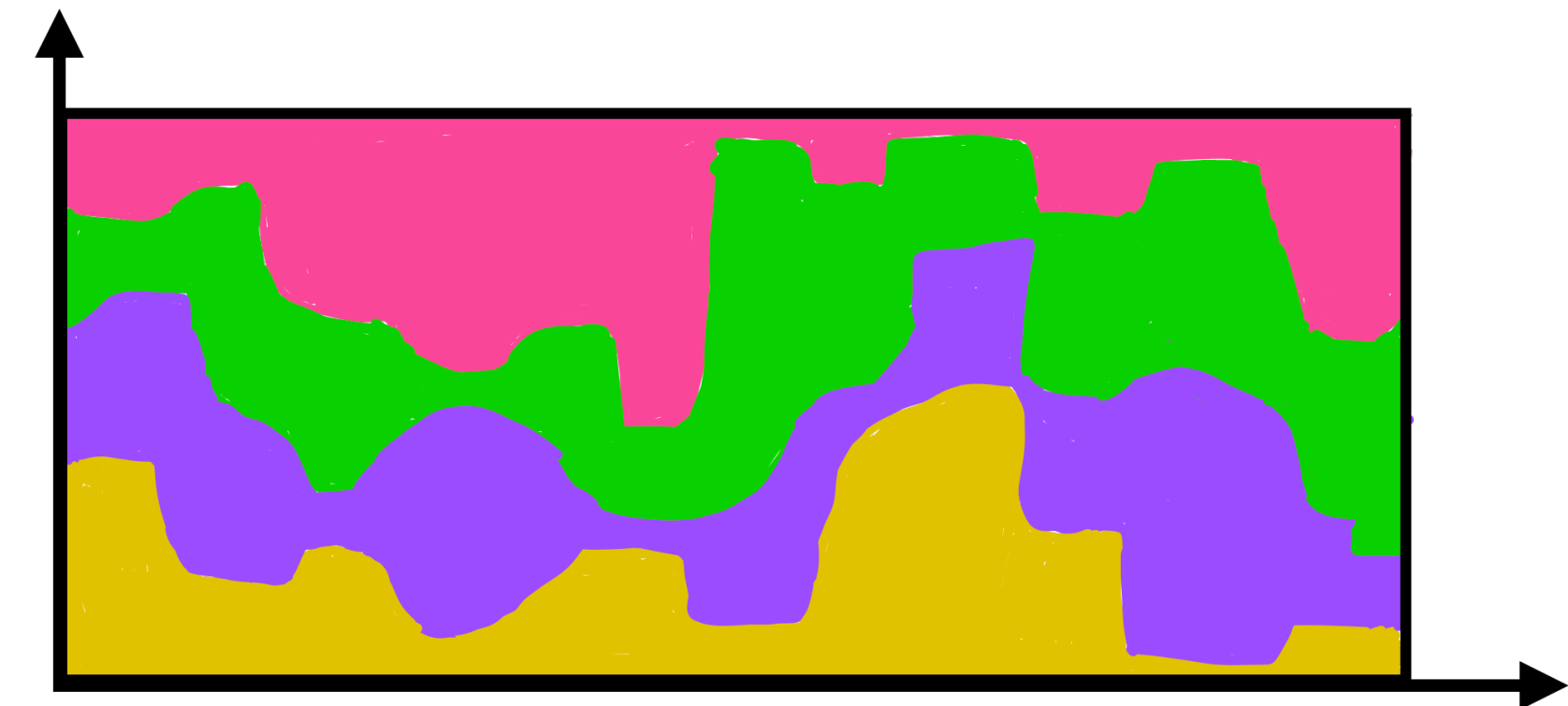
$$R_2 = r_{21}, r_{22}, r_{23}, r_{24}, \dots$$

$$R_3 = r_{31}, r_{32}, r_{33}, r_{34}, \dots$$

$$R_p = r_{p1}, r_{p2}, r_{p3}, r_{p4}, \dots$$



Stitch/pack the solutions together

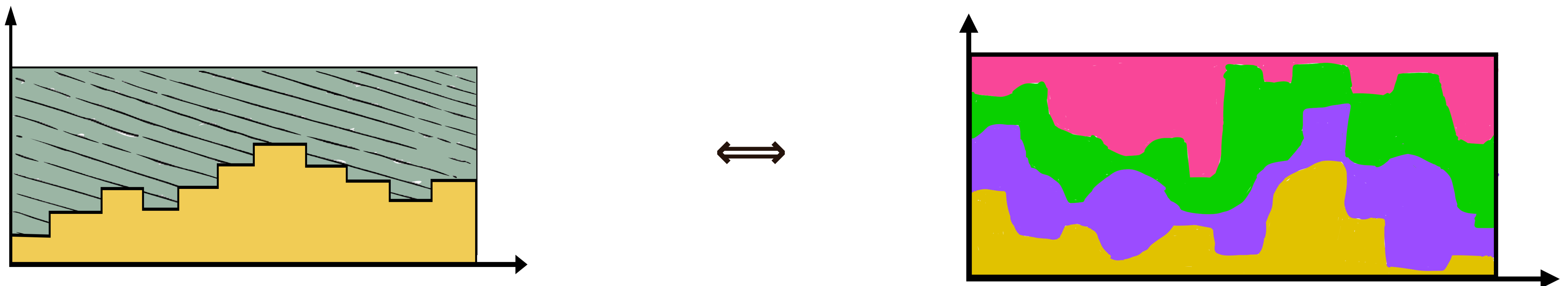


Theorem [Green paging upper bound \rightarrow Parallel paging upper bound]

Theorem [Green paging lower bound \rightarrow Parallel paging lower bound]

online green-paging alg w/ comp ratio $\Theta(\beta)$ \iff online \parallel -paging alg w/ comp ratio $\Theta(\beta)$

(with $O(1)$ resource augmentation)



Theorem: No deterministic online green-paging algorithm ($k_{\max}=k$ and $k_{\min}=k/p$) can be $o(\log P)$ -competitive.

Theorem: No deterministic online green-paging algorithm ($k_{\max}=k$ and $k_{\min}=k/p$) can be $o(\log P)$ -competitive.

\Rightarrow no parallel-paging algorithm can be $o(\log P)$ -competitive.

Theorem: No deterministic online green-paging algorithm ($k_{\max}=k$ and $k_{\min}=k/p$) can be $o(\log P)$ -competitive.

\Rightarrow no parallel-paging algorithm can be $o(\log P)$ -competitive.

Theorem: \exists a *universal green-paging solution* that is $O(\log P)$ -competitive for all request sequences.

Theorem: No deterministic online green-paging algorithm ($k_{\max}=k$ and $k_{\min}=k/p$) can be $o(\log P)$ -competitive.

\Rightarrow no parallel-paging algorithm can be $o(\log P)$ -competitive.

Theorem: \exists a *universal green-paging solution* that is $O(\log P)$ -competitive for all request sequences.

$\Rightarrow \exists$ *universal $O(\log P)$ -competitive parallel-paging algorithm.*

Bounds for Green Paging and Parallel Paging

[Agrawal, Bender, Das, Kuszmaul, Peserico, Scquizzato SODA 2021, SPAA 2022]

Theorem: No online green-paging algorithm ($k_{\max}=k$ and $k_{\min}=k/p$) can be $o(\log P)$ -competitive.

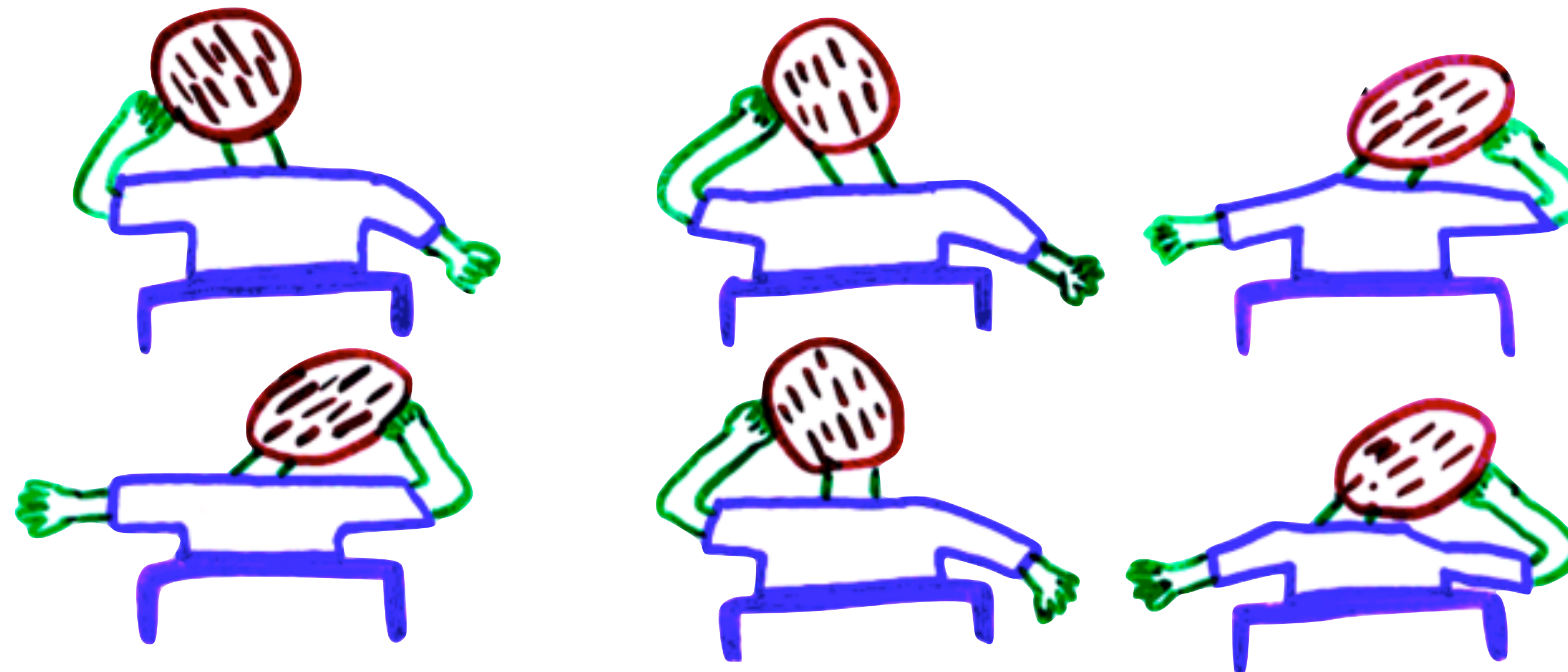
\Rightarrow no parallel paging algorithm can be $o(\log P)$ -competitive.

Theorem: \exists a *universal green-paging solution* that is $O(\log P)$ -competitive for all request sequences.

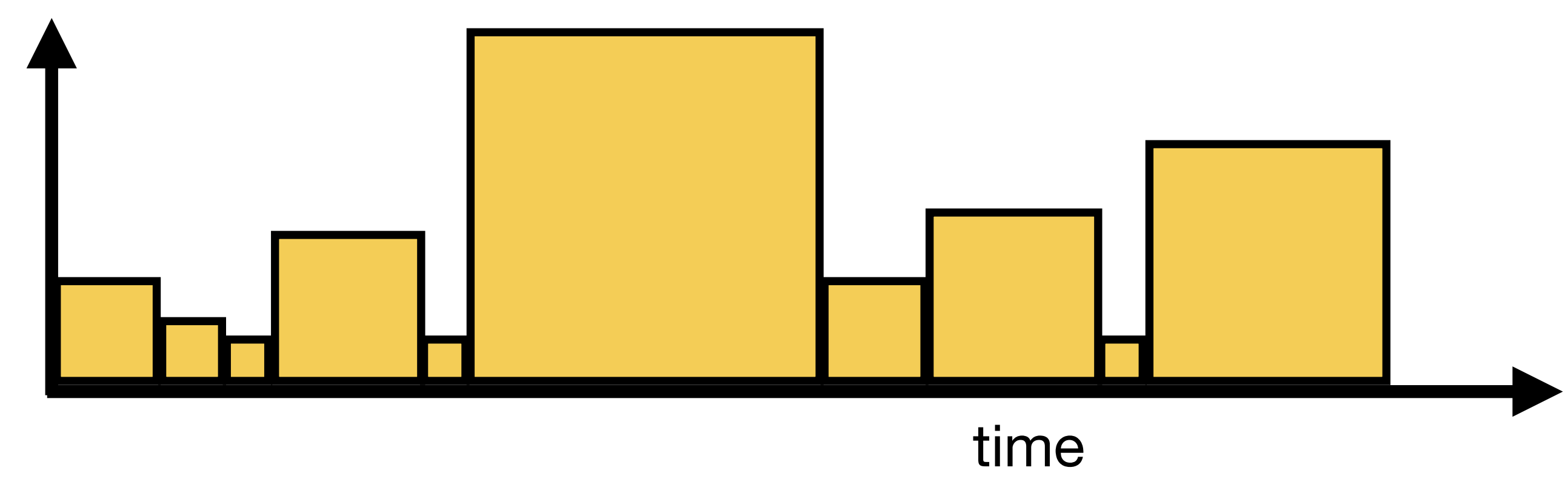
$\Rightarrow \exists$ *universal $O(\log P)$ -competitive parallel-paging algorithm.*



Here's where I skip how to make the universal solution.



Green-paging competitive ratio: $O(\log p)$ -competitive universal algorithm

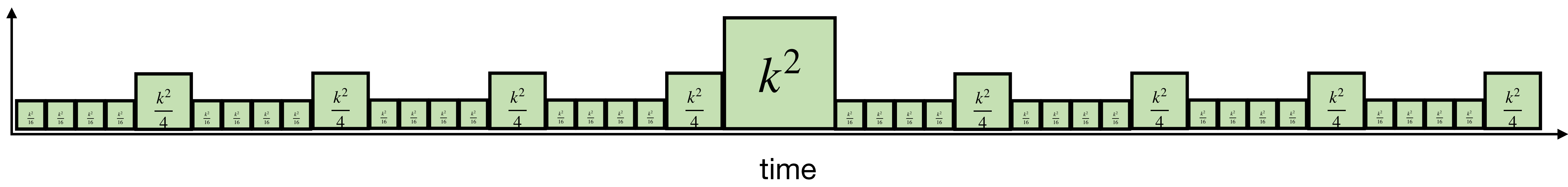
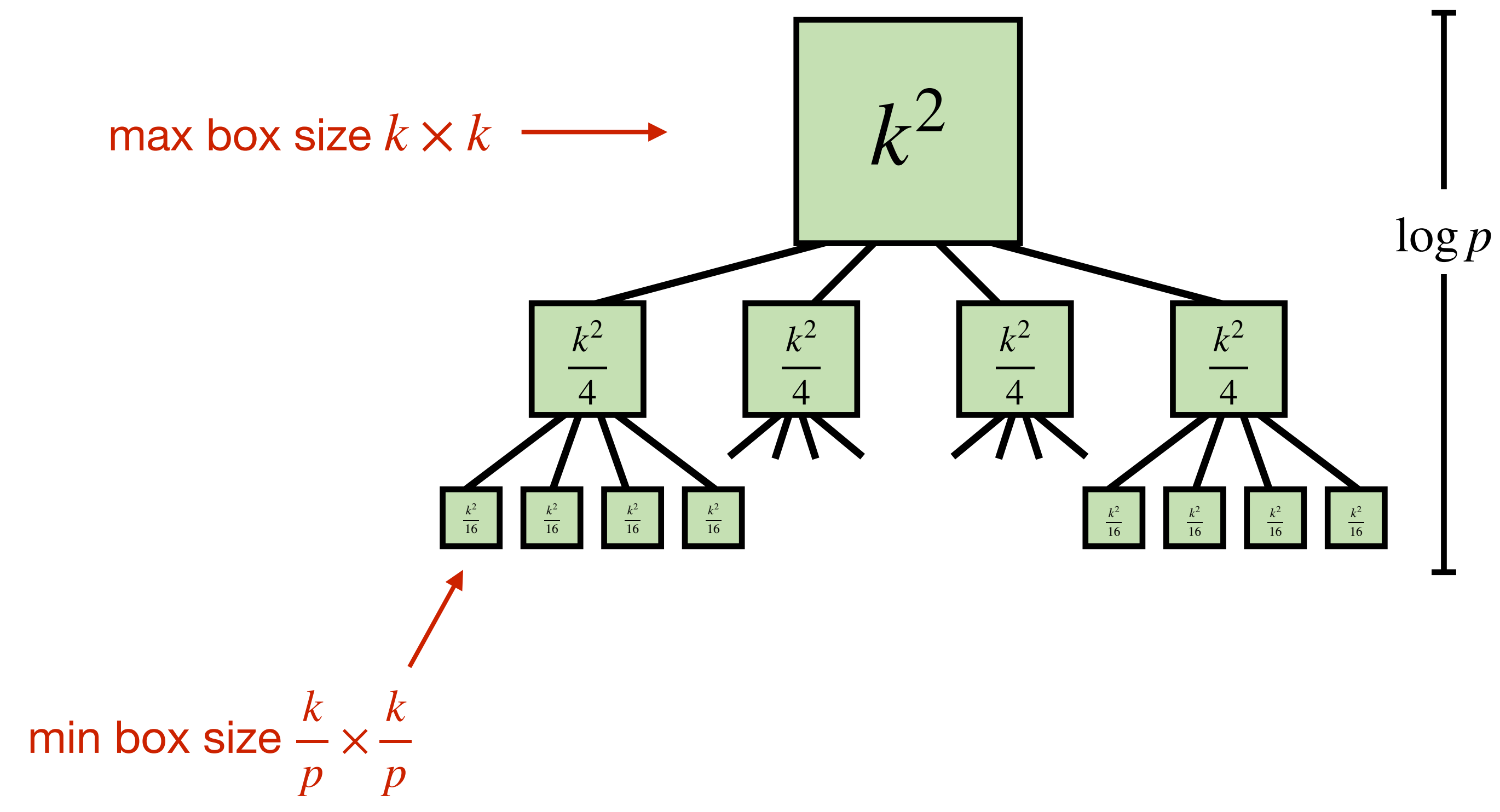


Thm: one can approximate any green-paging solution with a box-profile solution for the same asymptotic cost.

[Bender, Ebrahimi, Fineman, Ghasemiesfeh, Johnson, McCauley SODA 2014]

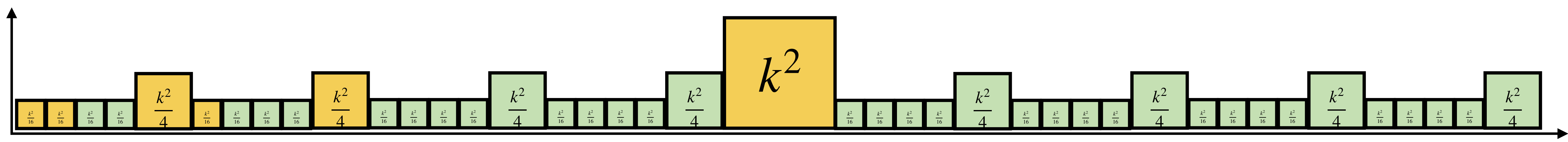
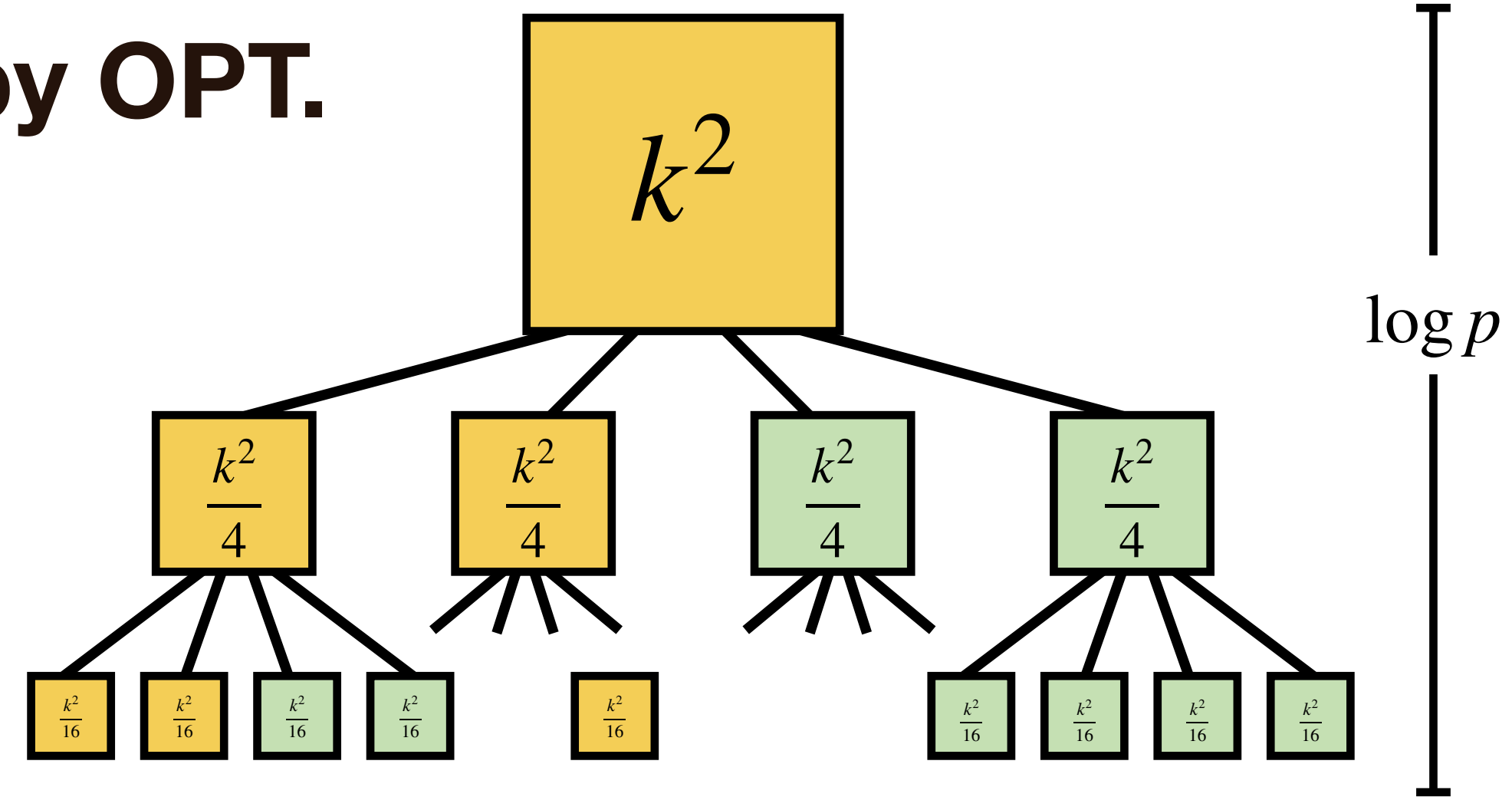
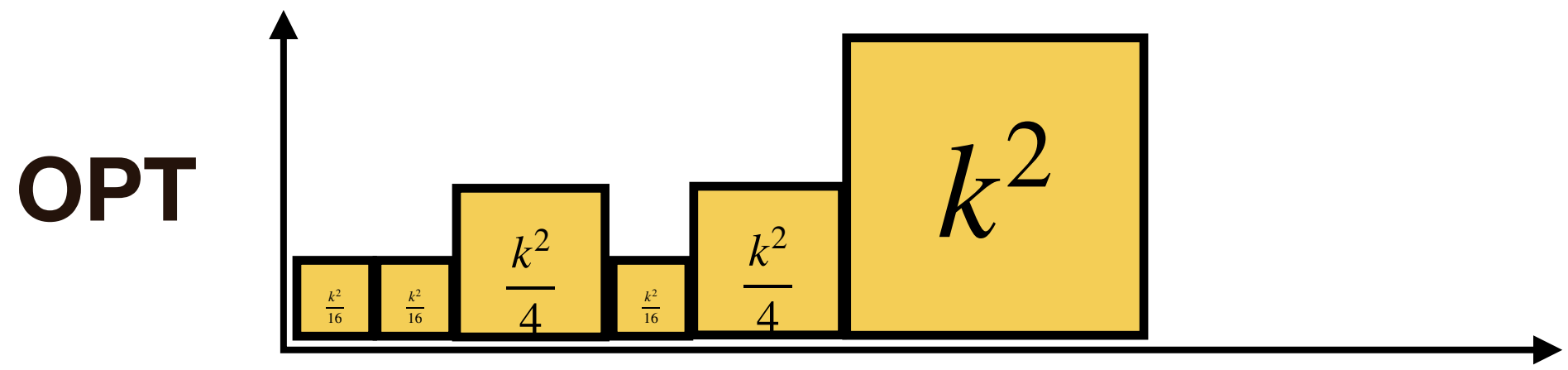
Green-paging competitive ratio: $O(\log p)$ -competitive universal algorithm

The universal green-paging memory profile is a *repeated post-order traversal* of this tree.



Green-paging competitive ratio: $O(\log p)$ -competitive universal algorithm

at least one box in root-to-leaf path is utilized by OPT.



Optimist versus pessimist.

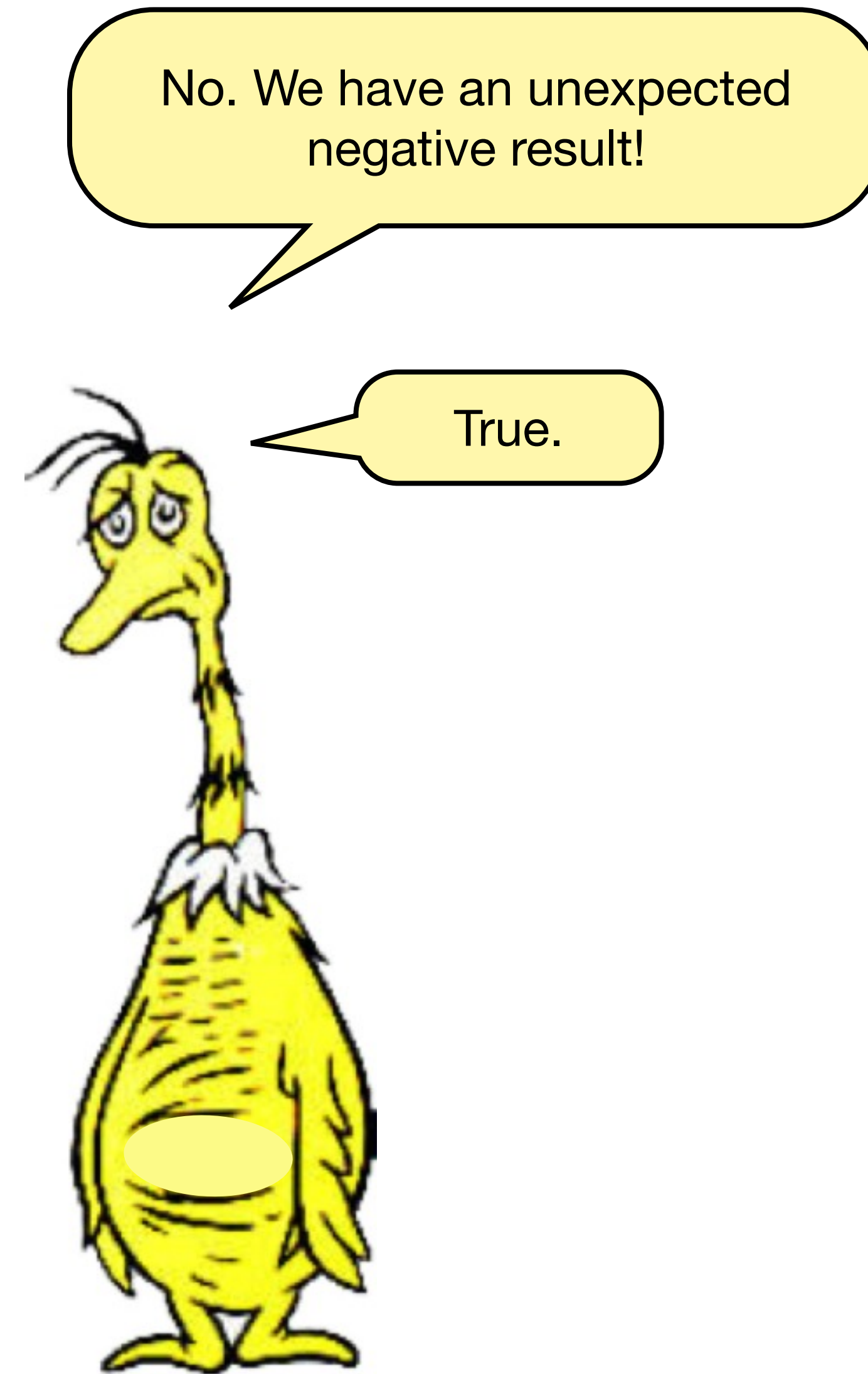
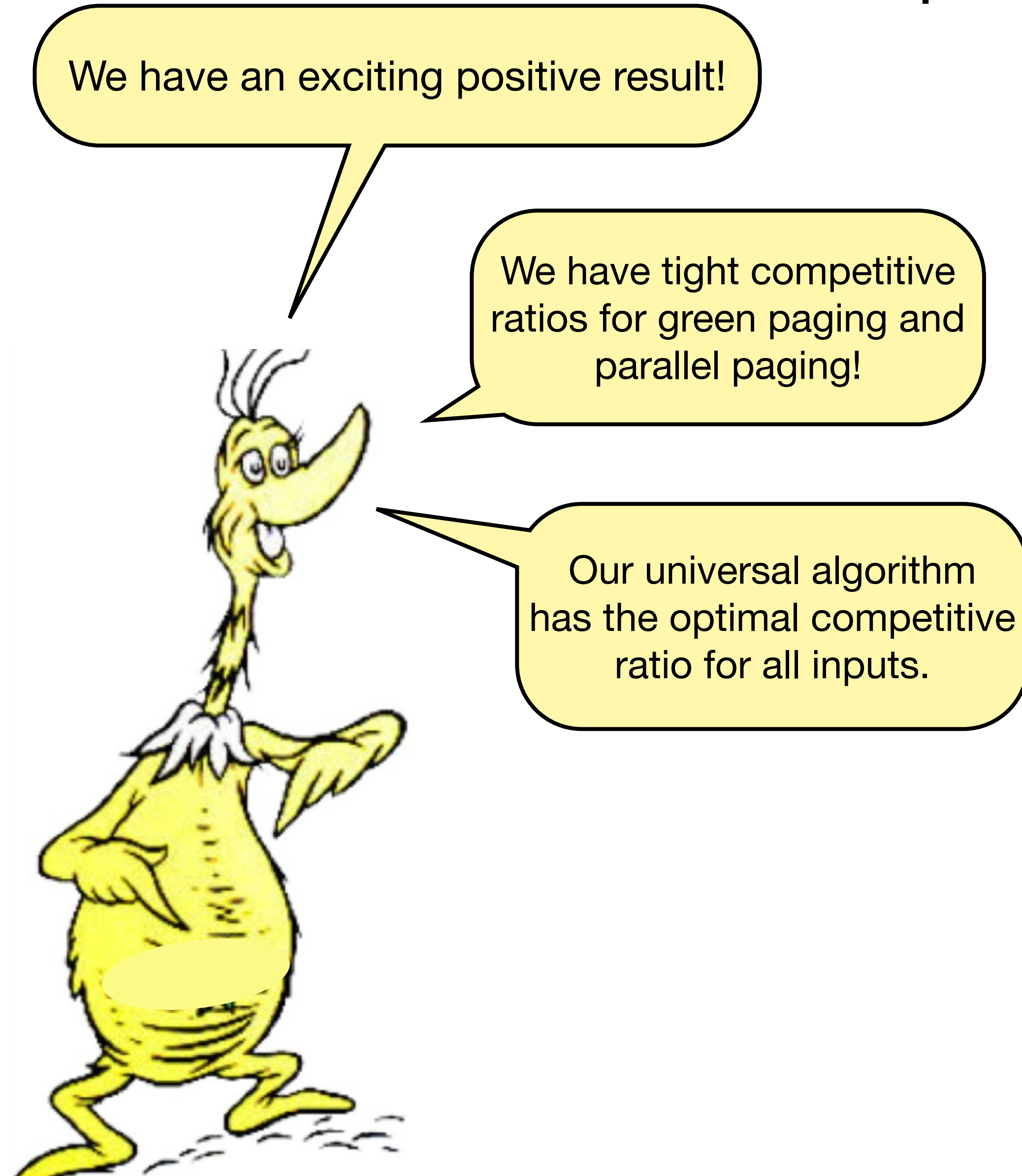
We have an exciting positive result!



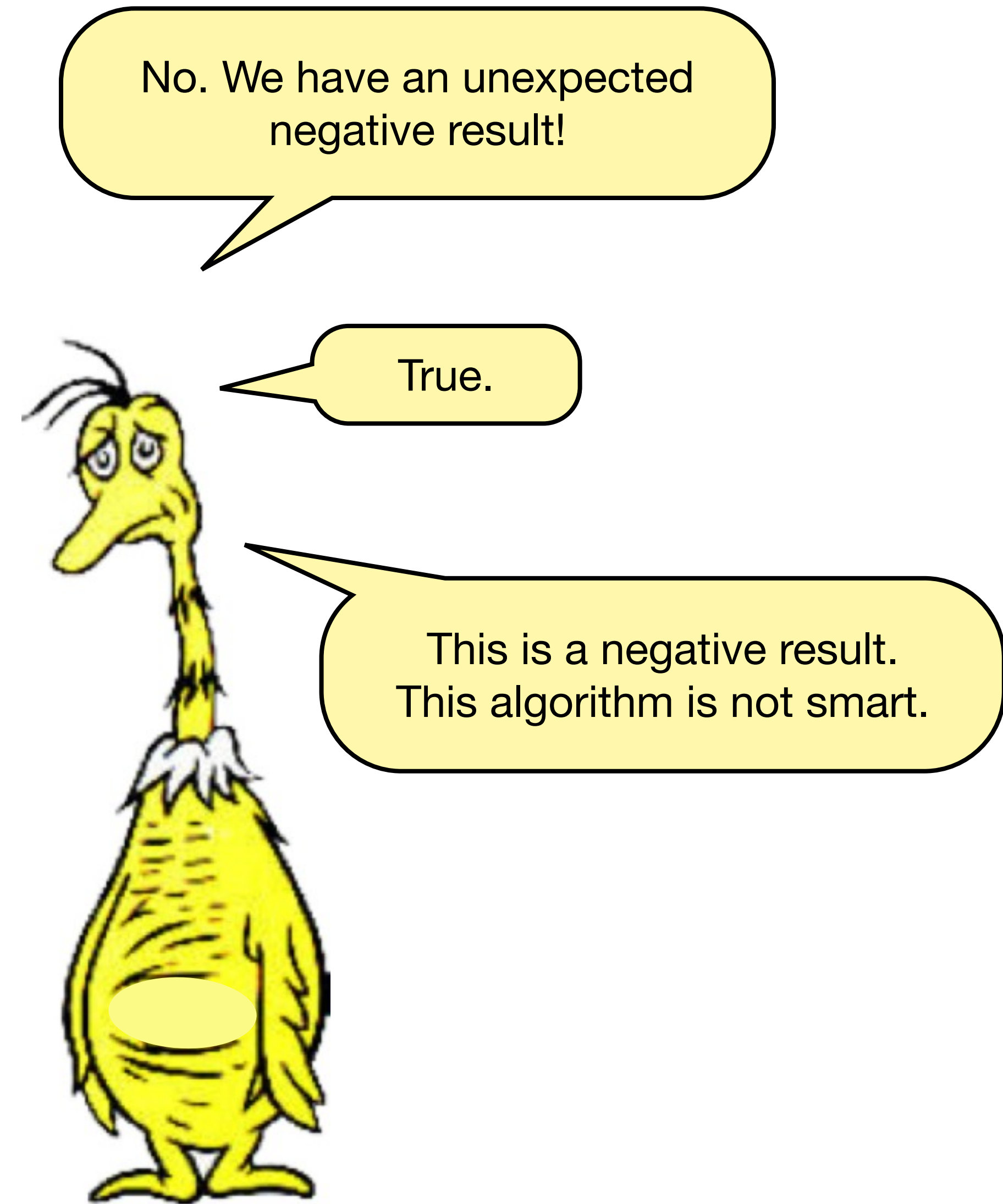
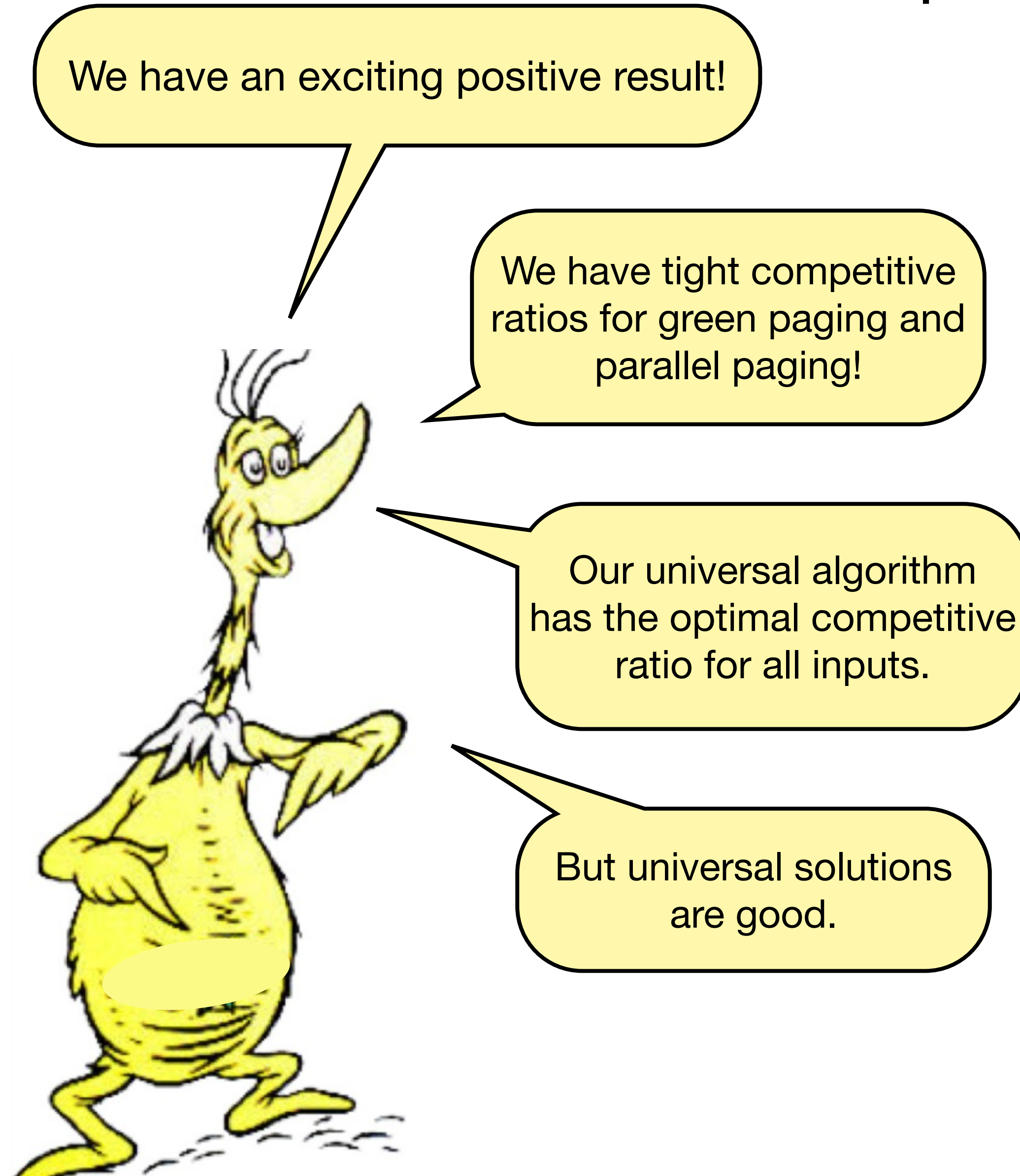
No. We have an unexpected negative result!



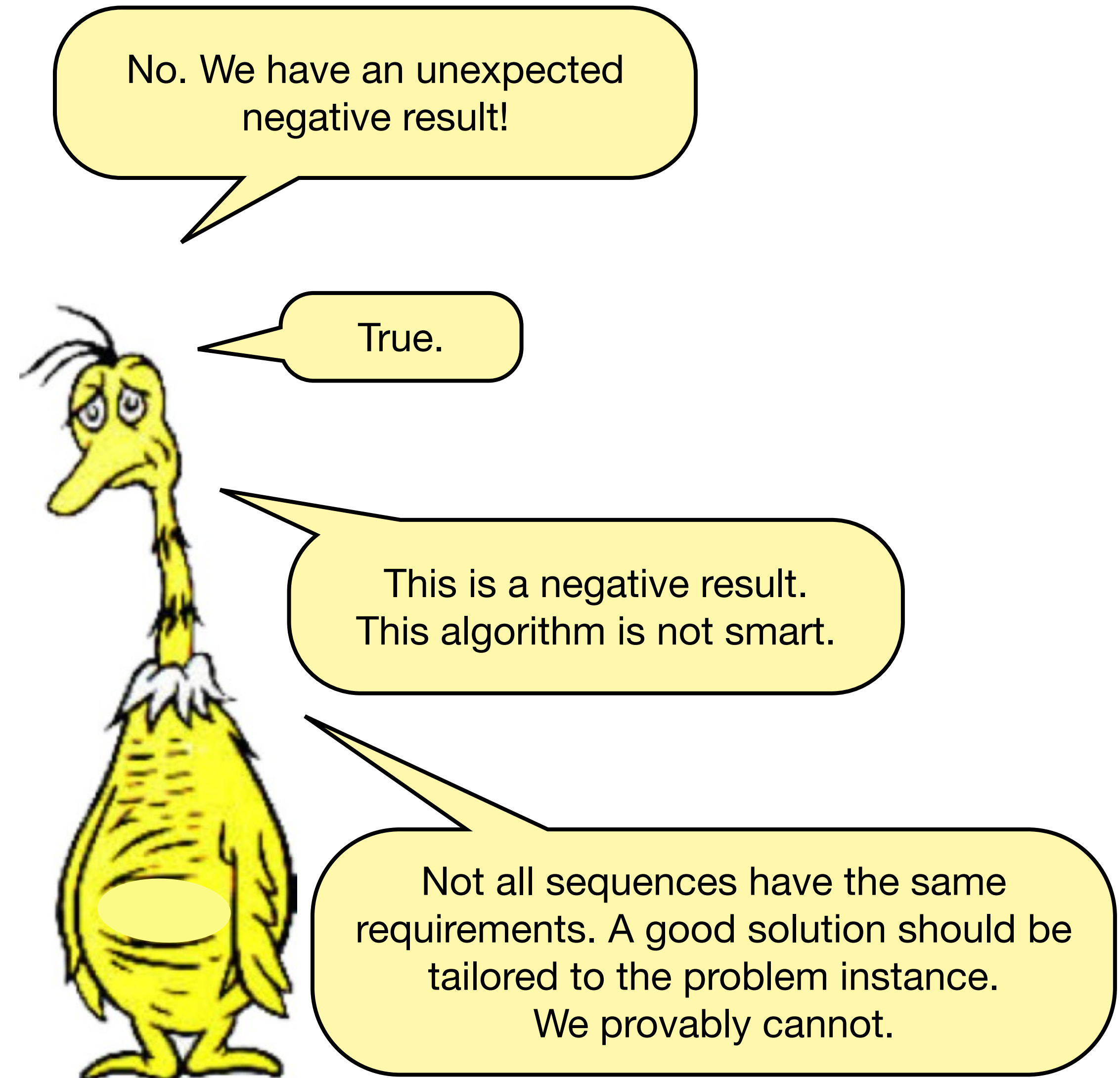
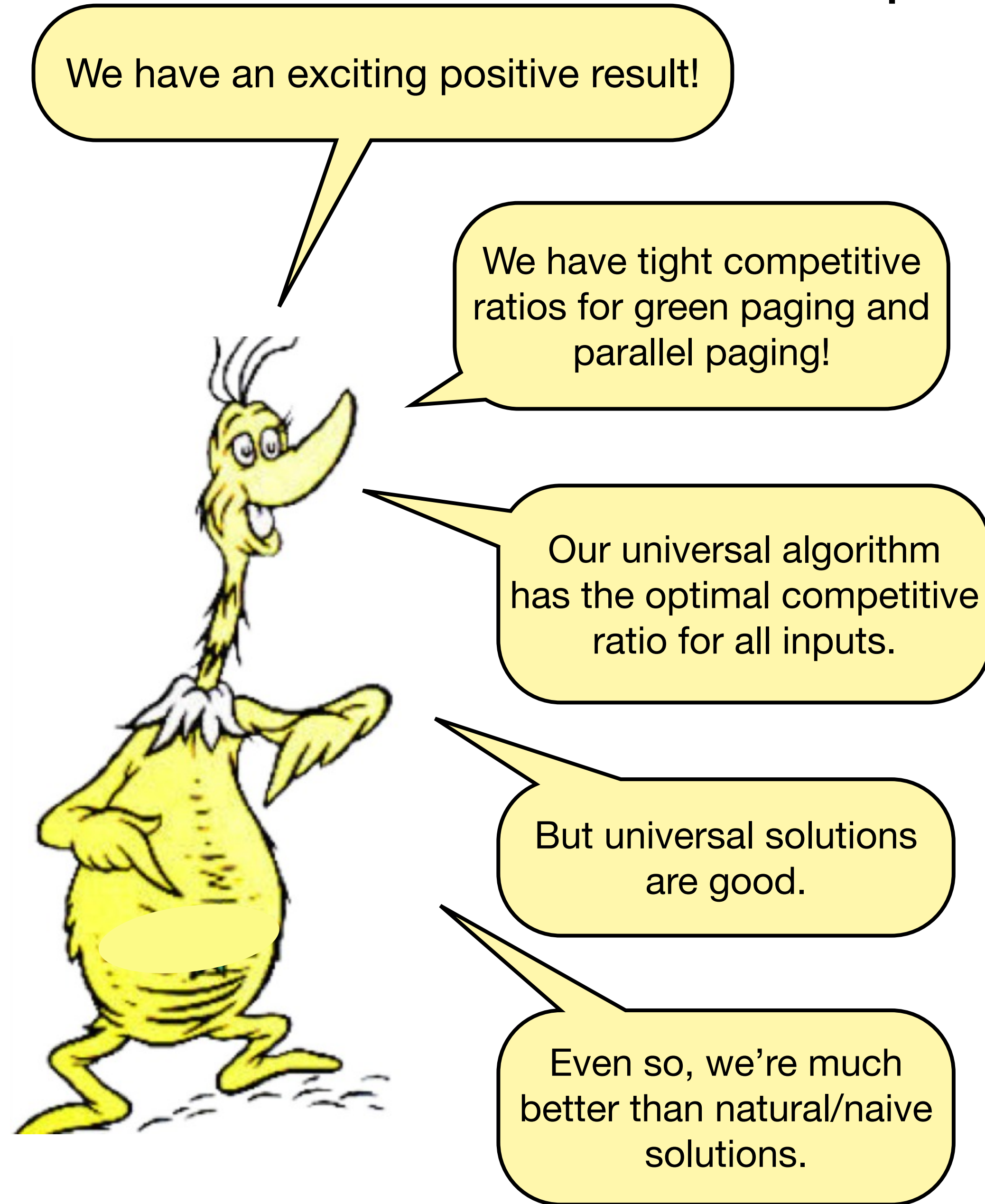
Optimist versus pessimist.



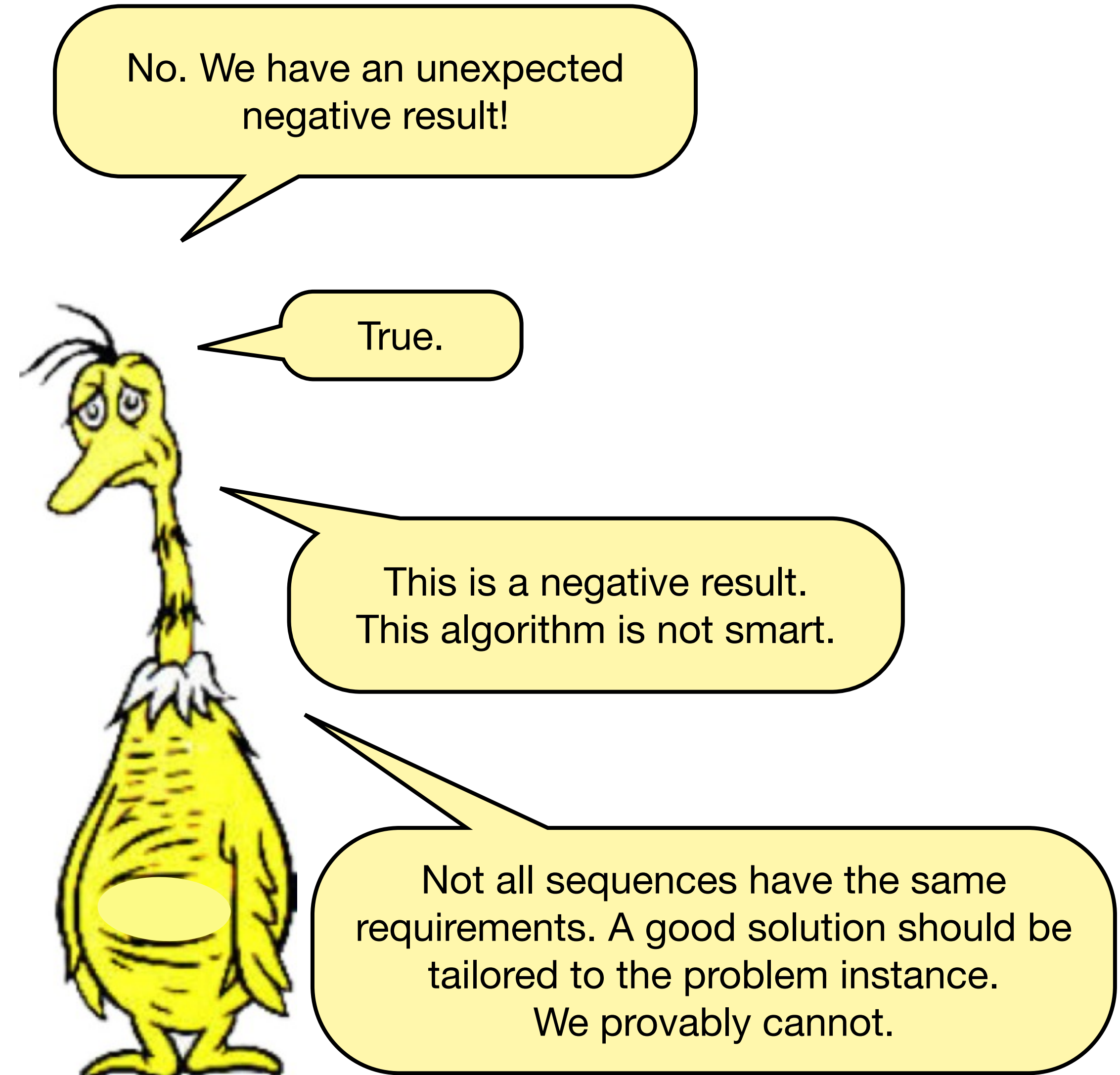
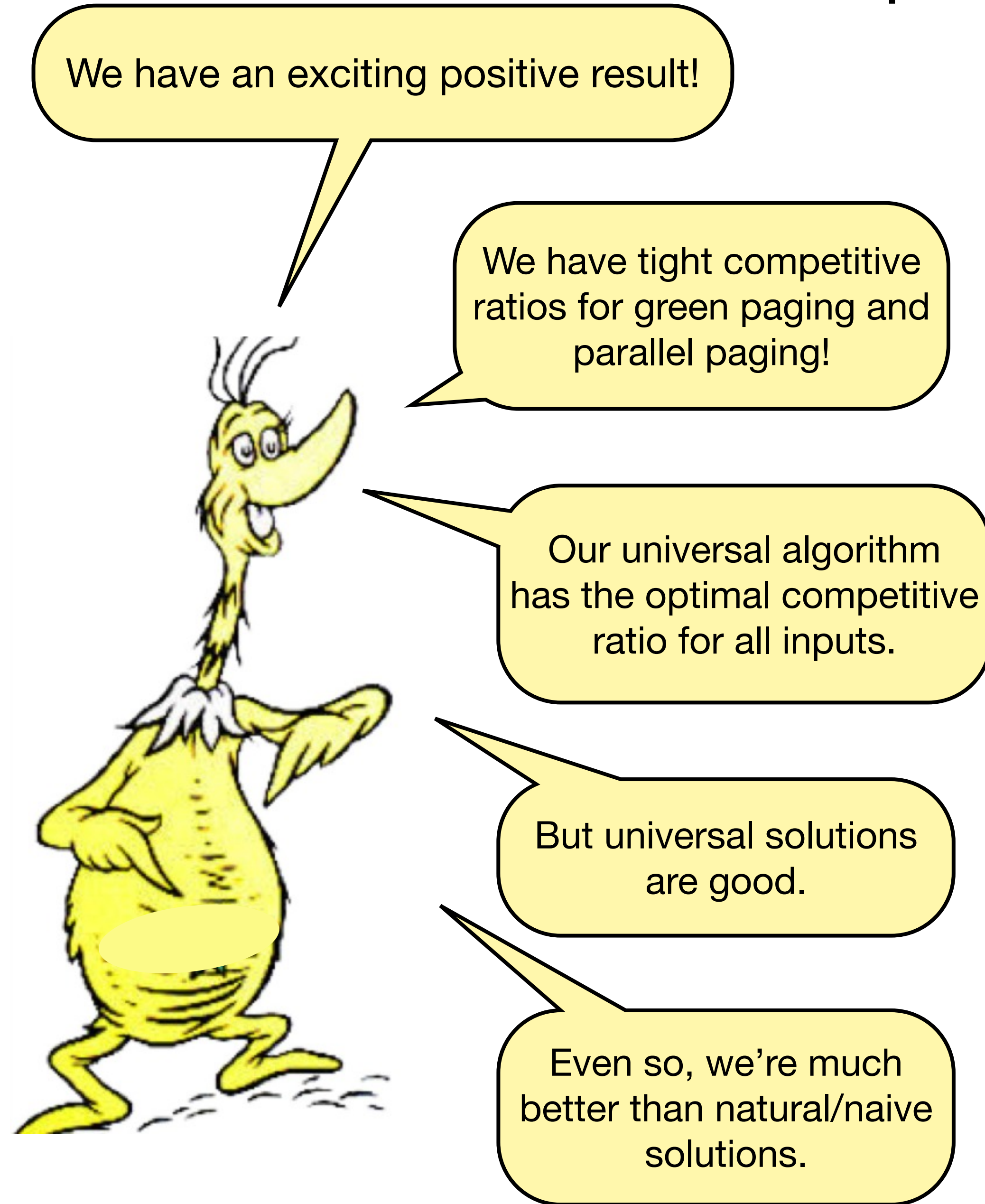
Optimist versus pessimist.



Optimist versus pessimist.



Optimist versus pessimist.



We now finally have the tools for reasoning about parallel paging.

- eg., green paging and the notion of cache impact

We should use these tools for beyond-worst-case analysis and in actual systems.