

# Formal Models for Concurrent Reconfiguration of Component Assemblies

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Vincent Lanore

December 10, 2015

Based on work done with Christian Pérez

Discrete Structures Day





Subset of Avalon working on  
**programming models**

- for cloud
- for computing grids
- for High-Performance Computing (HPC)...

People

- Christian Pérez
- Hélène Coullon
- Jérôme Richard
- Pedro Silva
- myself

Programming model:

- Idea
- **Formal specification**
  - formal syntax
  - formal semantics
- **Properties?**
- Implementation
- Evaluation on use cases
  - performance
  - code metrics

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## Benefits

- sturdier approach
- formal results
- connections with formal software engineering communities

- 1 Context
  - Component Models
  - Reconfiguration
- 2 The DIRECTMOD Component Model
- 3 Other Problems
- 4 Conclusion and Perspectives

## 1 Context

- Component Models
- Reconfiguration

## 2 The DIRECTMOD Component Model

## 3 Other Problems

## 4 Conclusion and Perspectives

# Component-Based Programming

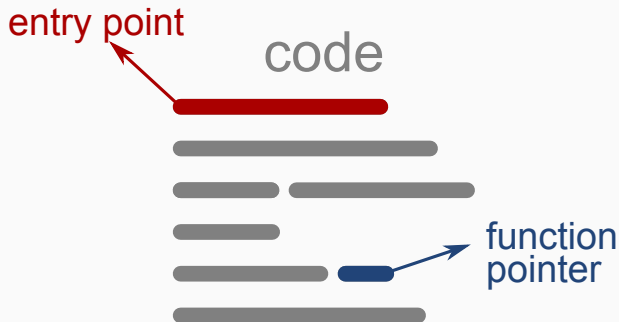
How to ease reuse by third parties?

code



# Component-Based Programming

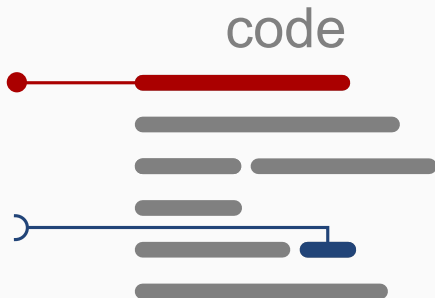
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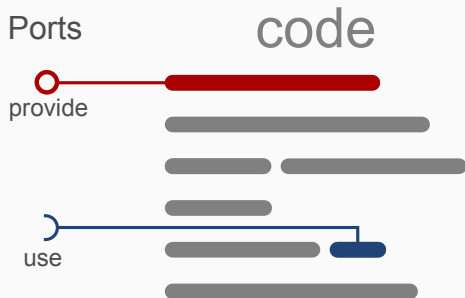
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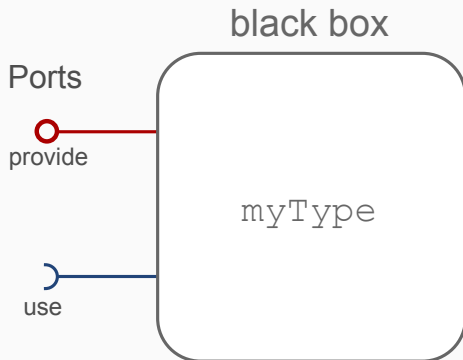
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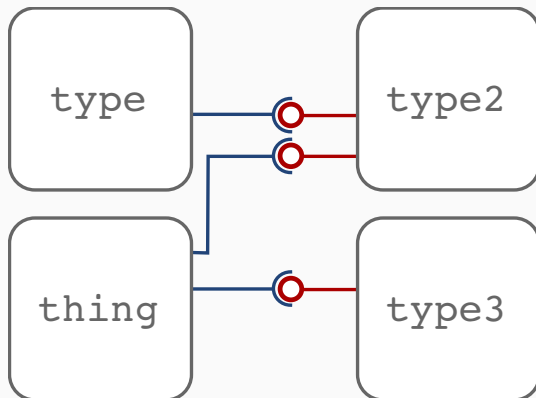
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→ a software component

# Component-Based Programming

How to ease reuse by third parties?



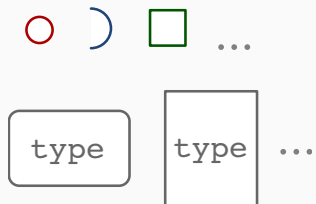
→ a component assembly

## Ports/component definitions

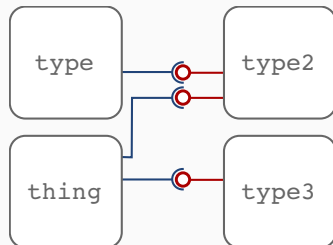


# Component Models

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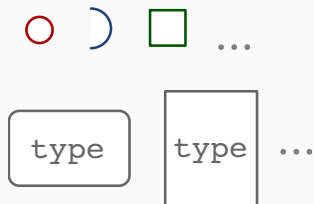


## + assembly model

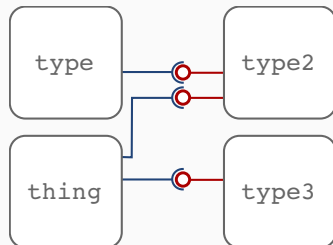


# Component Models

## Ports/component definitions



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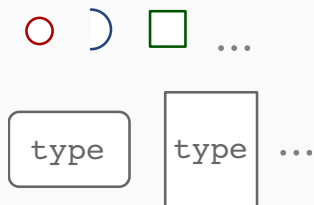


## Benefits

- reuse
- separation of concerns
- structure-level view

# Component Models

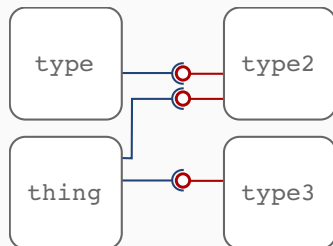
## Ports/component definitions



## Benefits

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- separation of concerns
- structure-level view

## + assembly model



## HPC Component Models

- Examples: CCA, L2C...



## Reconfigurable applications

application structure changes at runtime

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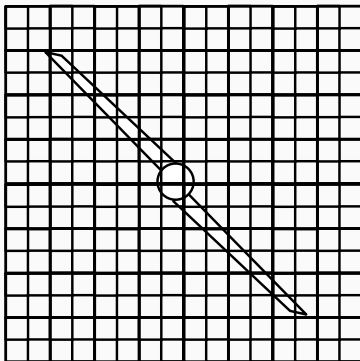
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## Reconfigurable applications

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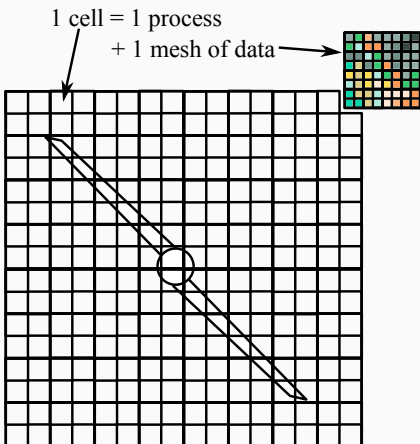
## Discretization



## Reconfigurable applications

application structure changes at runtime

**Example:** Adaptive Mesh Refinement (AMR)



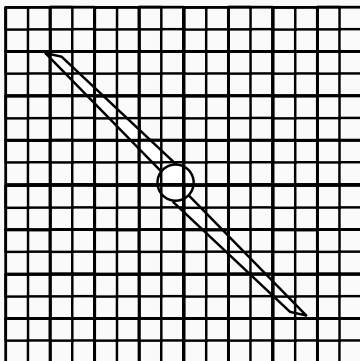
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- one process per cell

## Reconfigurable applications

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### Discretization



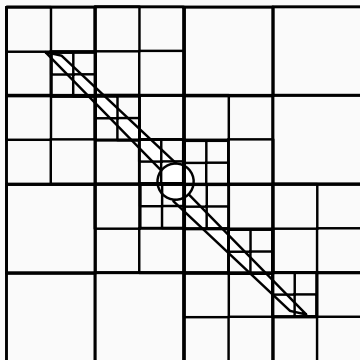
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## Reconfigurable applications

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**Example:** Adaptive Mesh Refinement (AMR)

### Variable resolution



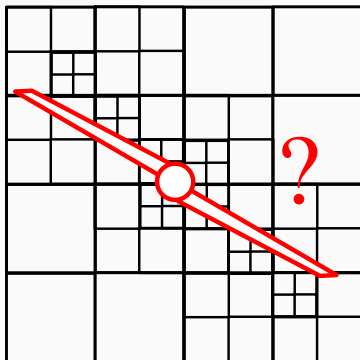
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## Reconfigurable applications

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### Variable resolution



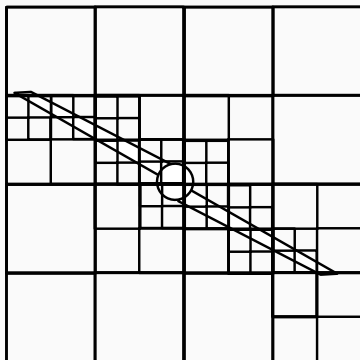
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## Reconfigurable applications

application structure changes at runtime

**Example:** Adaptive Mesh Refinement (AMR)

### Dynamic re-meshing



- fixed resolution cells
- one process per cell
- complex data structure
- dynamic process pool
- dynamic data structure

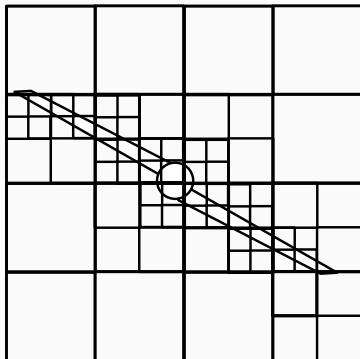


## Reconfigurable applications

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**Example:** Adaptive Mesh Refinement (AMR)

### Dynamic re-meshing

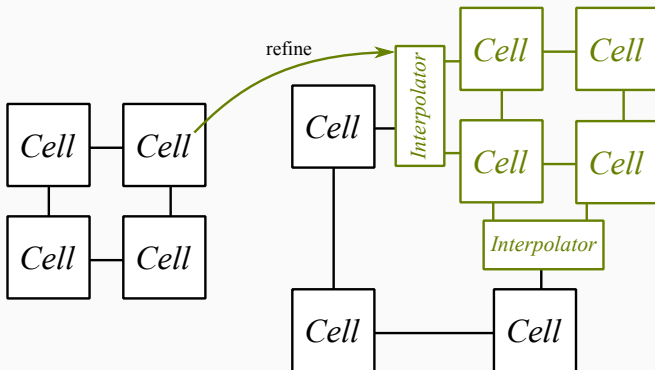


- fixed resolution cells
- one process per cell
- complex data structure
- dynamic process pool
- dynamic data structure

⇒ **complex for  
programmers**

# Structure-level Reconfiguration

One possible way to write AMR:



## Pro

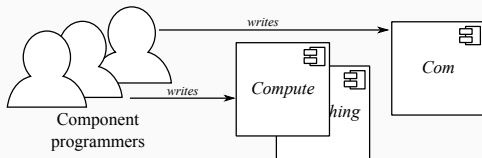
- structure-level reconfiguration

## Challenges

- application model
- performance

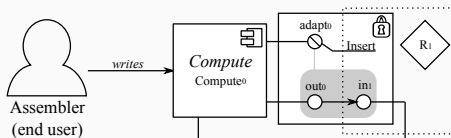
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# Programming Model Roles

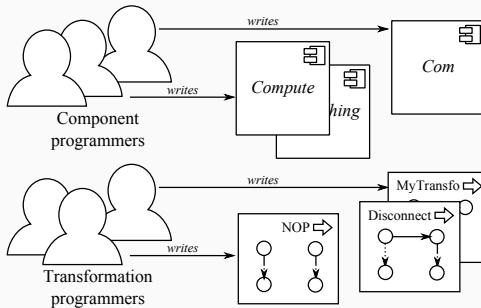


*reusable*

*application-specific*

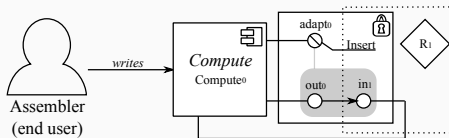


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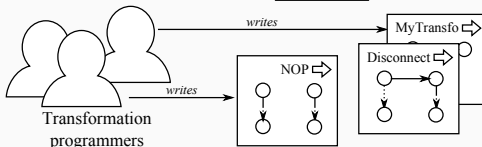
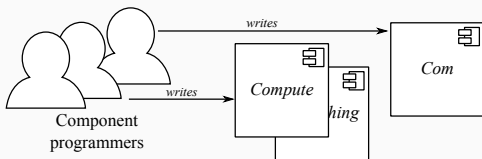


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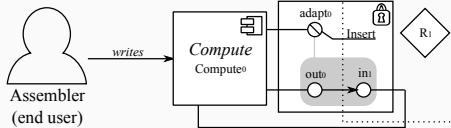
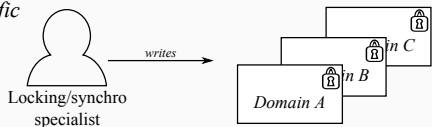


# Programming Model Roles



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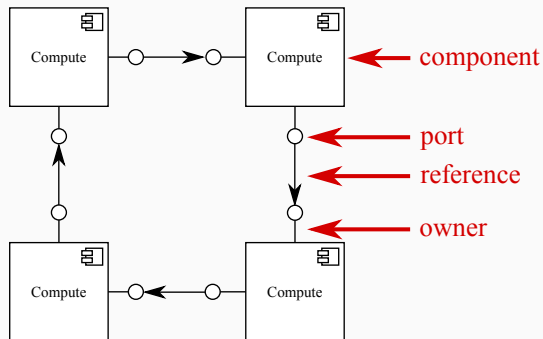


## Base component model

- non-reconfigurable
- similar to L2C/CCA
- call-stack-based operational semantics (see manuscript)
- resource model (see manuscript)

## DirectMOD: a full reconfigurable model

- additional concepts to
  - specify locking scope
  - specify reconfiguration
- extended syntax + reconfiguration semantics



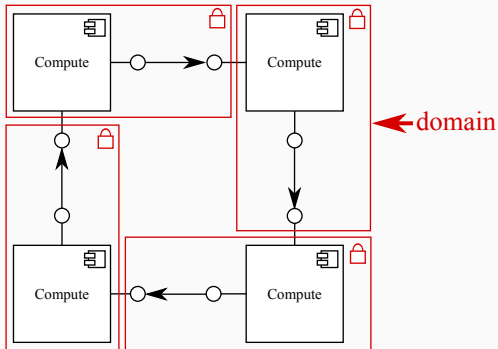
## Elements

- components
- ports

## Relations

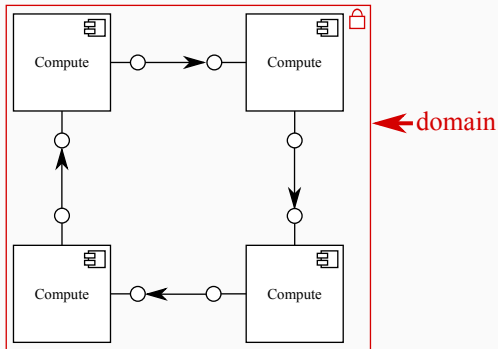
- point-to-point references
- owner





## New element: domains

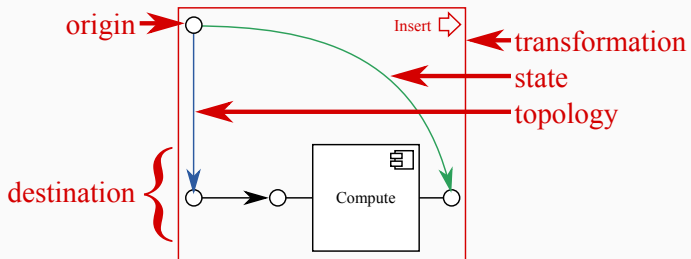
- manages a subassembly
- unit of locking
- reconfigure its contents
- user-defined scope



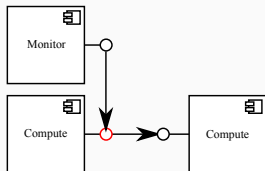
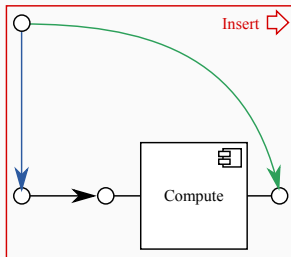
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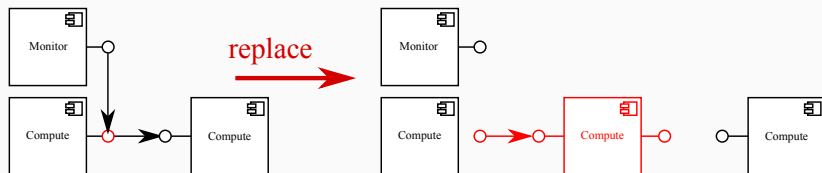
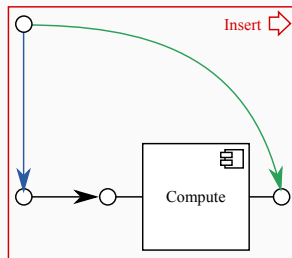
# Transformations



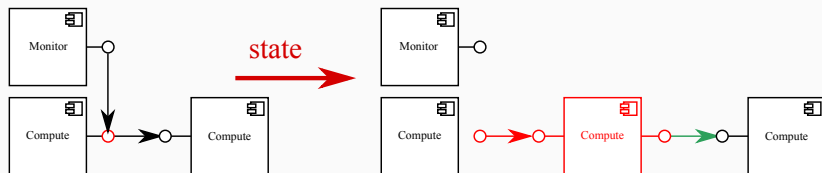
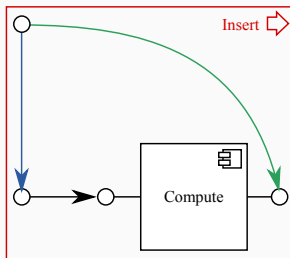
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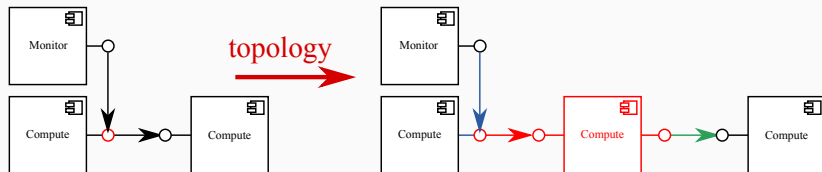
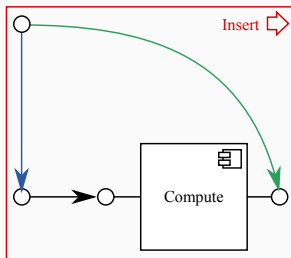
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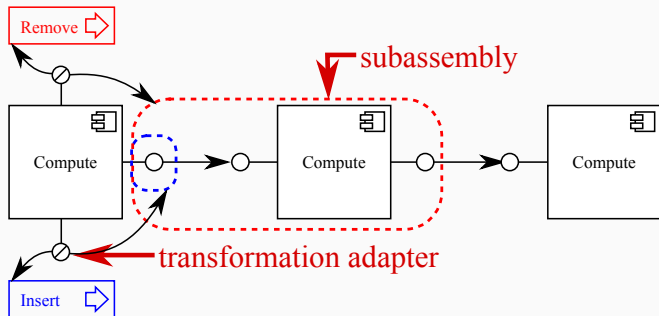
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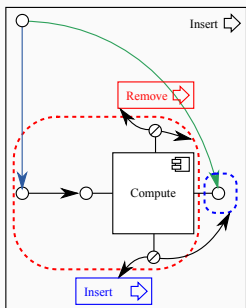
# Adapters



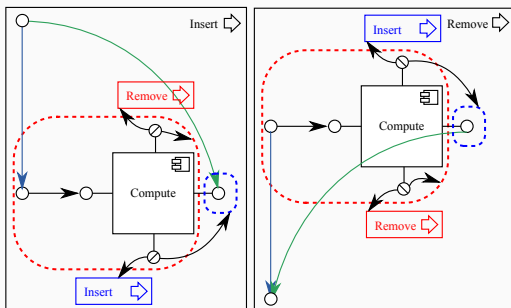
- special kind of port
- links transformation to its target



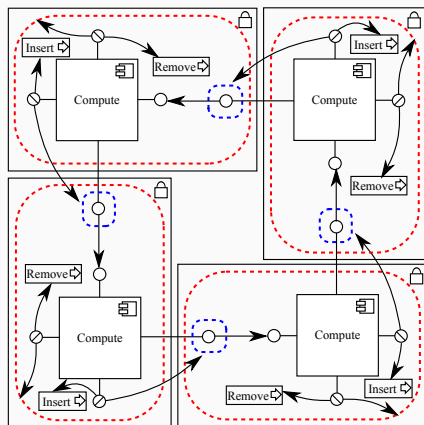
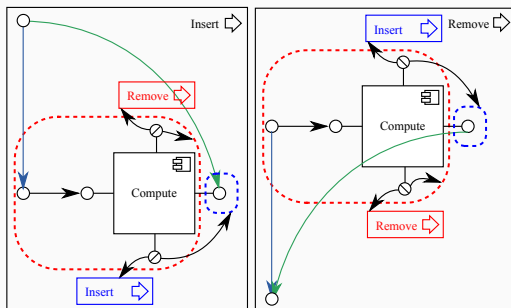
# Full DirectMOD Assembly



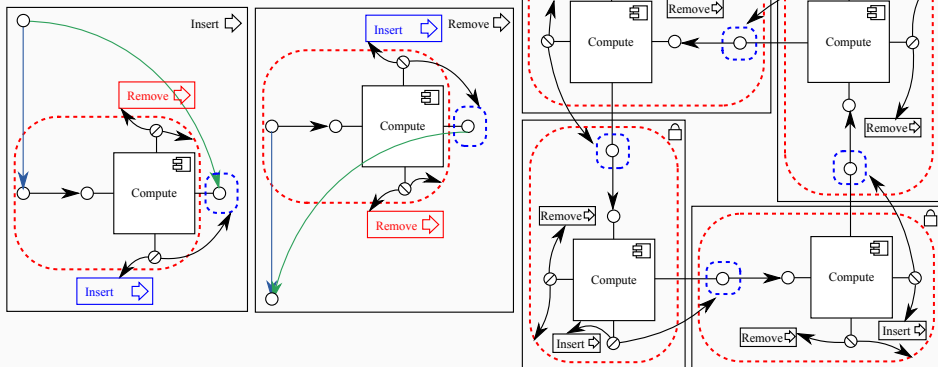
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# Full DirectMOD Assembly



- specify starting assembly
- assembly representation during runtime

## Definition Example: Ports

The set of DirectMOD ports on nameset  $\mathcal{N}$  is defined by:

$$\begin{aligned} Ports_{dmod}(\mathcal{N}) &= \{USE(name, ref) \mid (name, ref) \in \mathcal{N}^2\} \\ &\cup \{PROVIDE(name) \mid name \in \mathcal{N}\} \\ &\cup \{ADAPT(name, transfo) \mid (name, transfo) \in \mathcal{N}^2\} \end{aligned} \quad (1)$$

where :

- provide and use ports are defined as for the preliminary model;
- the  $name \in \mathcal{N}$  in the *ADAPT* operator is the name of the adapter;
- $transfo \in \mathcal{N}$  is the transformation reference of the adapter;
- $\alpha$  is the target assembly.

## Example Definition: Transformations

Let  $\mathcal{A}$  be a set of assemblies and  $\mathcal{N}$  a set of names. A *transformation*  $\tau$  is of the form:

$$\tau = (\textit{name}, \alpha, \omega, s, t) \tag{2}$$

where:

- $\textit{name} \in \mathcal{N}$  is the name of the transformation;
- $\alpha \in \mathcal{A}$  is the *origin* of the transformation;
- $\omega \in \mathcal{A}$  is the *destination* of the transformation;
- $s : \textit{Support}(\alpha) \rightarrow \textit{Support}(\omega) \cup \{\perp\}$  is the *state mapping*;
- $t : \textit{Support}(\alpha) \rightarrow \textit{Support}(\omega) \cup \{\perp\}$  is the *topology mapping*.

## The DirectMOD model

- specialized graph structure
  - components, ports, resources
  - concurrent semantics
- transformations

## Challenges and perspectives

- proofs with transformations
- locking / deadlock detection

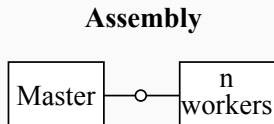
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# SpecMOD Principle

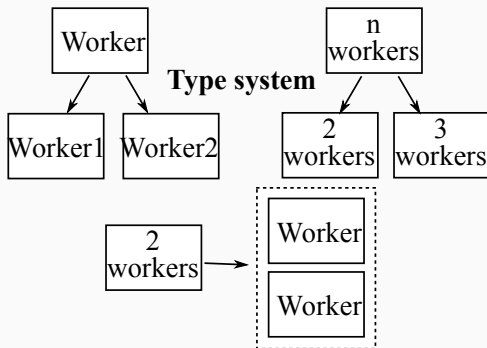
## Very simple assembly model

- components (squares)
- endpoints (circles)
- edges



## Rich type system

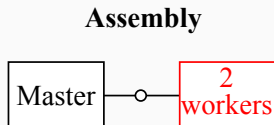
- component types
- endpoint types
- specialization relations



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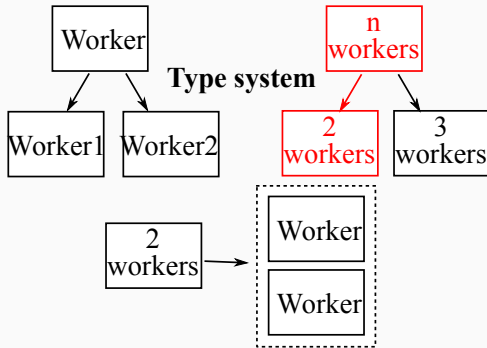
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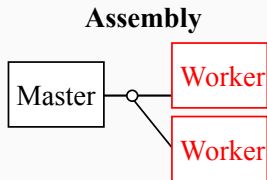
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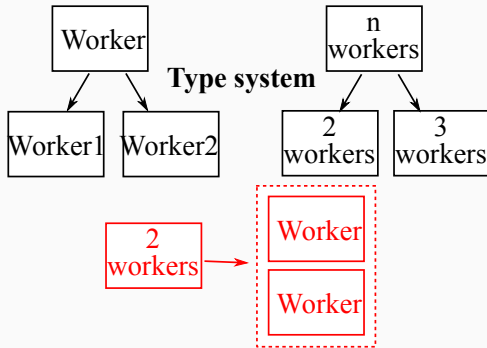
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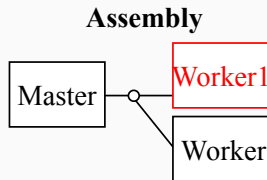
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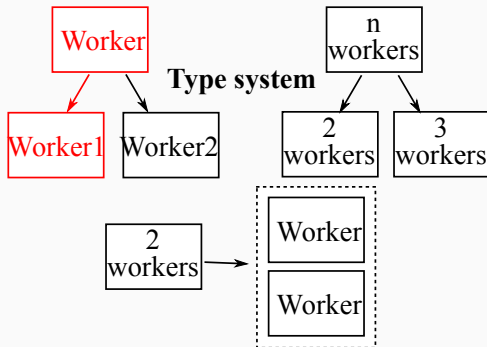
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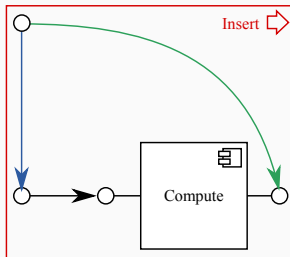
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## SpecMOD: a general specialization calculus

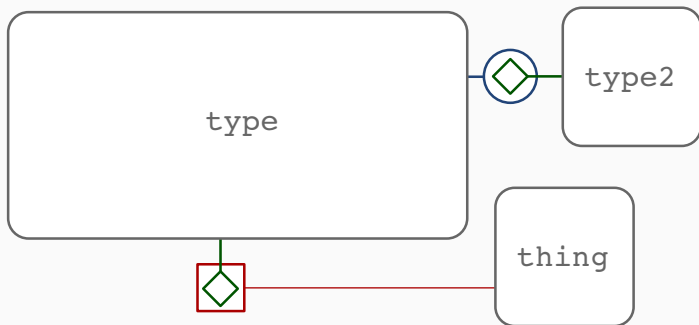
- type system definition
- assembly definition
- specialization operations as rewriting rules
- parametric type systems encoding

# Encoding Example





Components implemented by component assemblies  
called composite components



## Structure

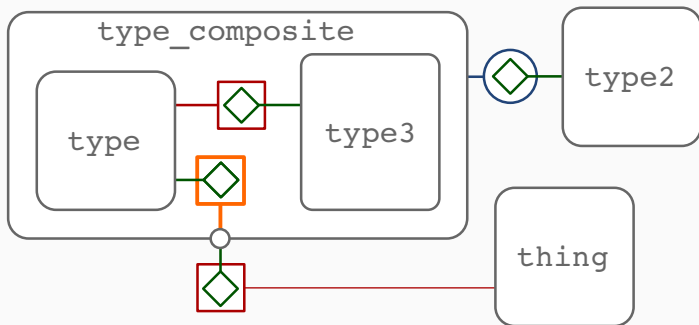
- bigraphs?

## Challenges

- transformation expression



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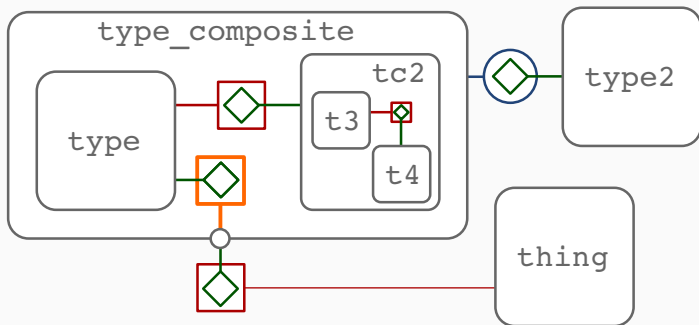
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## Structure

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## Discrete structures themes:

- specialized graph-like structures
  - multi-sorted graphs
  - list edges
  - hierarchy
- graph transformations
- graph specialization through rewriting

## Problems and perspectives

- proofs with transformations
- locking / deadlock detection
- encoding / decoding complex structures